MZ-N707

SERVICE MANUAL

Ver 1.0 2002. 01



US Model Canadian Model AEP Model UK Model

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| Model Name Using Similar Mechanism | NEW |
|------------------------------------|---------------|
| Mechanism Type | MT-MZN707-177 |
| Optical Pick-up Name | LCX-5R |

SPECIFICATIONS

MD Recorder

Audio playing system

MiniDisc digital audio system Laser diode properties

Material: GaAlAs MQW

Wavelength: $\lambda = 790 \text{ nm}$

Emission duration: continuous

Laser output: less than $44.6 \mu W$

(This output is the value measured at a distance of 200 mm from the lens surface on the optical

pick-up block with 7 mm aperture.) Recording and playback time

When using MDW-80

Maximum 160 min. in monaural

Maximum 320 min. in stereo

Revolutions

Approx. 380 rpm to 2,700 rpm (CLV)

Error correction

ACIRC (Advanced Cross Interleave Reed

Solomon Code)

Sampling frequency

44.1 kHz

Sampling rate converter

Input: 32 kHz/44.1 kHz/48 kHz

Coding

ATRAC (Adaptive TRansform Acoustic

Coding)

ATRAC3 — LP2

ATRAC3 - LP4

- Continued on next page -

PORTABLE MINIDISC RECORDER

9-873-458-01 2002A0500-1 © 2002.1

Sony Corporation

Personal Audio Company Published by Sony Engineering Corporation



Modulation system

EFM (Eight to Fourteen Modulation)

Number of channels

2 stereo channels

1 monaural channel

Frequency response 20 to 20,000 Hz ± 3 dB

Wow and Flutter

Below measurable limit

Inputs

Microphone: stereo mini-jack, minimum input level 0.35 mV

Line in: stereo mini-jack, minimum input level 49 mV

Optical (Digital) in: optical (digital) mini-jack

Output

: stereo mini-jack, maximum output level 5mW+5mW, load impedance 16 ohm (except US model) 1) The battery life may be shorter due to operating 5mW+5mW, load impedance 24 ohm (US model)

Power requirements

Sony AC Power Adaptor connected at the DC IN 3 V jack (country model in parentheses): 120 V AC, 60 Hz (USA, Canada) 230 V AC, 50/60 Hz (Continental Europe)

230 - 240 V AC, 50 Hz (U.K.) Nickel Cadmium rechargeable battery NC-

WMAA (supplied) LR6 (size AA) alkaline battery (not supplied)

Dimensions Approx. $81 \times 74.4 \times 27.7 \text{ mm (w/h/d) } (3^{1/4} \times 3)$

$\times 1^{1/8}$ in.) without projections.

Mass Approx. 116 g (4.1 oz) the recorder only

Supplied accessories

NC-WMAA Nickel Cadmium rechargeable battery (1)

AC power adaptor (1)

Headphones/earphones with a remote control (1)

Battery charging stand (1)

Optical cable (1)

USB cable (1) CD-ROM (1)*

Battery carrying case (1)

Carrying pouch (1) (US and Canadian models) Carrying case with a belt clip (1) (except US model) Blank Minidisc (1) (US and Canadian models) Car connecting pack (1) (US and Canadian models) Car battery cord (1) (US and Canadian models)

* Do not play a CD-ROM on an audio CD player.

Design and specifications are subject to change without notice.

Battery life1)

When recording²⁾

(Unit: approx.hours)(JEITA3))

| Batteries | SP Stereo | LP2 Stereo | LP4 Stereo |
|-----------------------------------------------------------------------|--------------|---------------|---------------|
| NC-WMAA Nickel Cadmium rechargeable battery ⁴⁾ | 4 | 6 | 7.5 |
| LR6 (SG) Sony alkaline dry battery ⁵⁾ | 9 | 13 | 16 |

- conditions, the temperature of the location, and varieties of batteries.
- 2) When you record, use a fully charged rechargeable battery. Recording time may differ according to the alkaline batteries.
- 3) Measured in accordance with the JEITA (Japan Electronics and Information Technology Industries Association) standard.
- 4) When using a 100% fully charged rechargeable battery.
- 5) When using a Sony LR6 (SG) "STAMINA" alkaline dry battery (produced in Japan).

When playing

(Unit: approx.hours)(JEITA1))

| | | / \ | , |
|-----------------------------------------------------------------------|--------------|---------------|---------------|
| Batteries | SP Stereo | LP2 Stereo | LP4 Stereo |
| NC-WMAA Nickel Cadmium rechargeable battery ²⁾ | 15 | 16 | 20 |
| LR6 (SG) Sony alkaline dry battery ³⁾ | 42 | 48 | 56 |

- 1) Measured in accordance with the JEITA (Japan Electronics and Information
- Technology Industries Association) standard. ²⁾ When using a 100% fully charged rechargeable battery.
- 3) When using a Sony LR6 (SG) "STAMINA" alkaline dry battery (produced in Japan).

SAFETY-RELATED COMPONENT WARNING!!

COMPONENTS IDENTIFIED BY MARK A OR DOTTED LINE WITH MARK A ON THE SCHEMATIC DIAGRAMS AND IN THE PARTS LIST ARE CRITICAL TO SAFE OPERATION. REPLACE THESE COMPONENTS WITH SONY PARTS WHOSE PART NUMBERS APPEAR AS SHOWN IN THIS MANUAL OR IN SUPPLEMENTS PUB-LISHED BY SONY.

ATTENTION AU COMPOSANT AYANT RAPPORT À LA SÉCURITÉ!

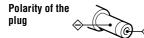
LES COMPOSANTS IDENTIFIÉS PAR UNE MARQUE A SUR LES DIAGRAMMES SCHÉMATIQUES ET LA LISTE DES PIÈCES SONT CRITIQUES POUR LA SÉCURITÉ DE FONCTIONNEMENT. NE REMPLACER CES COM-POSANTS QUE PAR DES PIÈCES SONY DONT LES NUMÉROS SONT DONNÉS DANS CE MANUEL OU DANS LES SUPPLÉMENTS PUBLIÉS PAR SONY.

CAUTION

Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.

On power sources

- Use house current, Nickel Cadmium rechargeable battery, LR6 (SG) battery, or car battery.
- For use in your house: Use the AC power adaptor supplied with this recorder. Do not use any other AC power adaptor since it may cause the recorder to malfunction.



Notes on chip component replacement

- Never reuse a disconnected chip component.
- Notice that the minus side of a tantalum capacitor may be damaged by heat.

Flexible Circuit Board Repairing

- Keep the temperature of the soldering iron around 270 °C during repairing.
- Do not touch the soldering iron on the same conductor of the circuit board (within 3 times).
- Be careful not to apply force on the conductor when soldering or unsoldering.

UNLEADED SOLDER

Boards requiring use of unleaded solder are printed with the leadfree mark (LF) indicating the solder contains no lead.

(Caution: Some printed circuit boards may not come printed with the lead free mark due to their particular size)

🛂 : LEAD FREE MARK

Unleaded solder has the following characteristics.

- Unleaded solder melts at a temperature about 40 °C higher than ordinary solder.
 - Ordinary soldering irons can be used but the iron tip has to be applied to the solder joint for a slightly longer time.
 - Soldering irons using a temperature regulator should be set to about 350 $^{\circ}\text{C}$.
 - Caution: The printed pattern (copper foil) may peel away if the heated tip is applied for too long, so be careful!
- Strong viscosity
 - Unleaded solder is more viscous (sticky, less prone to flow) than ordinary solder so use caution not to let solder bridges occur such as on IC pins, etc.
- Usable with ordinary solder
 It is best to use only unleaded solder but unleaded solder may also be added to ordinary solder.

TABLE OF CONTENTS

| 1. | SERVICING NOTES | 4 |
|--------------|---------------------------------------------------------------------------|----|
| 2. | GENERAL | |
| 3. | DISASSEMBLY | |
| 3-1. | Disassembly Flow | |
| 3-2. | Case (Lower) | |
| 3-3. | Upper Panel Section | |
| 3-4. | LCD Module, Upper Panel Sub Assy | |
| 3-5. | Mechanism Deck (MT-MZN707-177) | |
| 3-6. | Set Chassis (5192) Assy | |
| 3-7. | MAIN Board | 9 |
| 3-8. | OP Service Assy (LCX-5R) | 10 |
| 3-9. | Holder Assy | 1 |
| 3-10. | DC Motor (Sled) (M602) | 1 |
| 3-11. | DC SSM18B Motor (Spindle) (M601), | |
| | DC Motor (Over Write Head Up/Down) (M603) | 12 |
| 4. | TEST MODE | 13 |
| _ | ELECTRICAL AD HIGHMENTS | |
| 5. | ELECTRICAL ADJUSTMENTS | 19 |
| 6. | DIAGRAMS | |
| 6-1. | Block Diagram – SERVO/USB Section – | 32 |
| 6-2. | Block Diagram – AUDIO Section – | 33 |
| 6-3. | Block Diagram – DISPLAY/KEY CONTROL/ | |
| | POWER SUPPLY Section – | 34 |
| 6-4. | Note for Printed Wiring Board and | _ |
| | Schematic Diagrams | 3: |
| 6-5. | Printed Wiring Board | 2 |
| | - MAIN Board (Component Side) | 30 |
| 6-6. | Printed Wiring Board | 2. |
| 6-7. | – MAIN Board (Conductor Side) – Schematic Diagram – MAIN Board (1/4) – | |
| 6-7. 6-8. | Schematic Diagram – MAIN Board (1/4) – | |
| 6-9. | Schematic Diagram – MAIN Board (3/4) – | |
| | Schematic Diagram – MAIN Board (4/4) – | |
| | IC Pin Function Description | |
| 0-11. | Te T III T uncuon Description | 7 |
| 7. | EXPLODED VIEWS | |
| 7-1. | Upper Panel, Case (Lower) Section | |
| 7-2. | Chassis Section | |
| 7-3. | MAIN Board Section | 5 |
| 7-4. | Mechanism Deck Section-1 (MT-MZN707-177) | |
| 7-5. | Mechanism Deck Section-2 (MT-MZN707-177) | 59 |
| 8. | ELECTRICAL PARTS LIST | 61 |

SECTION 1 SERVICING NOTES

NOTES ON HANDLING THE OPTICAL PICK-UP **BLOCK OR BASE UNIT**

The laser diode in the optical pick-up block may suffer electrostatic break-down because of the potential difference generated by the charged electrostatic load, etc. on clothing and the human body.

During repair, pay attention to electrostatic break-down and also use the procedure in the printed matter which is included in the

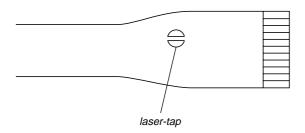
The flexible board is easily damaged and should be handled with care.

NOTES ON LASER DIODE EMISSION CHECK

Never look into the laser diode emission from right above when checking it for adjustment. It is feared that you will lose your sight.

NOTES ON HANDLING THE OPTICAL PICK-UP BLOCK (LCX-5R)

The laser diode in the optical pick-up block may suffer electrostatic break-down easily. When handling it, perform soldering bridge to the laser-tap on the flexible board. Also perform measures against electrostatic break-down sufficiently before the operation. The flexible board is easily damaged and should be handled with care.

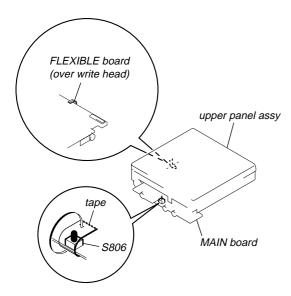


OPTICAL PICK-UP FLEXIBLE BOARD

• In performing the repair with the power supplied to the set, removing the MAIN board causes the set to be disabled. In such a case, fix a convex part of the open/close detect switch (S806 on MAIN board) with a tape in advance.

Handle the FLEXIBLE board (over write head) with care, as it has been soldered directly to the MAIN board.

In repairing the component side of MAIN board, connect the FLEXIBLE board (over write head) and the MAIN board with the lead wires in advance.



- · Replacement of CXD2677-202GA (IC801) used in this set requires a special tool.
- The shipment data will be cleared when the NV is reset. Therefore, change the NV adjusted values following the Change of NV Adjusted Values immediately after the NV was reset. (See page 19)
- This set requires the patch data in the nonvolatile memory (IC804) to be rewritten using the application, when the MAIN board or nonvolatile memory (IC804) was replaced. (See page

System requirements

IBM PC/AT or Compatible (The software does not run on Macintosh.) CPU: MMX™ Pentium® 233 MHz or higher (Pentium® II 400 MHz or higher is recommended.)

Hard disk drive space: 60 MB or more (The amount of necessary space depends on the version of the Windows OS or the size of your audio files.)

RAM: 64 MB or higher (128 MB or higher is recommended for Windows® XP Home Edition/Windows® XP Professional.)

CD-ROM drive (capable of digital playback by WDM)

Sound Board

USB port (supports USB 2.0 Full Speed (previously USB 1.1))

Operating System: Windows® 98/Windows® 98 Second Edition/Windows® 2000 Professional/Windows® Me/Windows® XP Home Edition/Windows® XP Professional (manufacturer installed)

The NTFS format of Windows® 2000 Professional, Windows® XP Home Edition, or Windows® XP Professional (manufacturer-installed) is supported only when used with the standard (factory) settings.

This software is not supported by the following environments.

- Windows® 95, Windows® NT, or other versions of Windows® NT (such as Server)
- An environment that is an upgrade of the original manufacturer-installed operating system, as in the following example

Windows® 3.1/Windows® 95 → Windows® 98 (or Windows® 98 Second Edition/ Windows® Me)

- Windows® Me / Windows® 2000 Professional → Windows® XP
- Multi-boot environment with Windows® 2000 (or Windows® XP) and Windows® 98 (or Windows® 98 Second Edition/Windows® Me)
- Display: High (16bit) Color or more (800 × 480 dot or more)
- Internet access: for Web registration and EMD services
- Internet access: for software upgrades and CDDB2 use. (US and Canadian models) $\,$
- Windows Media Player (version 7.0 or higher) installed for playing WMA files

- Trouble-free operation is not assured within a multiple-monitor environment.
- We do not assure trouble-free operation for all computers satisfying the system requirements.
- Trouble-free operation is not guaranteed following the self-conducted upgrade of home-built PCs or operating systems · We do not assure trouble-free operation of the system suspend, sleep, or hibernation function on
- all computers.

 For details, refer to "Net MD Help" of the online help.

The optical digital output connector (on computers provided with one) may be disabled during playback for the protection of copyrights

Notes on using OpenMG Jukebox with Windows 2000/Windows XP

If your computer is Windows 2000 Professional, Windows XP Home Edition, or Windows XP Professional, please be aware of the following before instaling OpenMG Inkehox

- 1 With Windows 2000 Professional, you must log on as "Administrators" (or with the user name "Administrator") to install OpenMG Jukebox.
- 2 With Windows XP Home Edition or Windows XP Professional, you must log on with user name "Computer Administrator" to install OpenMG Jubebox. To check whether a user name has the attribute of "Computer Administrator" or not, go to [Control Panel] - [User Account].

Notes on using OpenMG Jukebox with Windows XP/ Windows Me

If Windows XP/Windows Me is installed in your computer, and you perform the 'System Restore" function of the Windows "System Tools," the songs managed by OpenMG Jukebox may become corrupted and rendered unplayable Therefore, before executing "System Restore," back up the songs using "OpenMG

Jukebox Backup Tool" first.
Then, after the "System Restore" function is finished, restore the songs using "OpenMG Jukebox Backup Tool" to ensure the integrity and reliability of song playback

For more information about backup, refer to the online Help for OpenMG Jukebox.

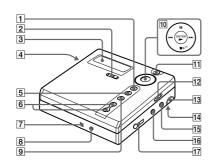
When songs become unplayable by executing "System Restore," an error dialog box may be displayed. In this case, follow the displayed messages

SECTION 2 GENERAL

This section is extracted from instruction manual.

Looking at the controls

The recorder



- 1 GROUP/CANCEL button
- 2 REC (record) switch
- 3 Display window
- 4 OPEN button
- 5 T MARK button
- 6 VOL +/- button The VOL + button has a tactile dot.
- 7 Terminal for attaching the battery charging stand
- 8 DC IN 3V jack
- 9 END SEARCH button
- 10 5 position control key

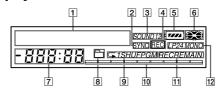
■ (stop)/CHG (charge) button

(stop)/CHG (charge) button

*The button has a tactile dot.

- 11 MENU button
- 12 HOLD switch
- 13 USB connecting jack
- 14 Battery compartment
- 15 LINE IN (OPTICAL) jack
- [16] MIC (PLUG IN POWER) jack
 There is a tactile dot left side of the MIC
 (PLUG IN POWER) jack.
- [17] (headphones/earphones) jack

The display window of the recorder

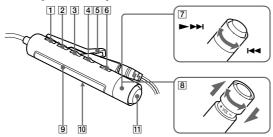


- 1 Character information display Displays the disc and track names, error messages, track numbers, etc.
- 2 SYNC (synchro-recording) indication
- 3 Sound indication
- 4 REC indication Lights up while recording. When flashing, the recorder is in record standby mode.
- 5 Battery level indication Shows approximate battery condition.
- 6 Disc indication Shows that the disc is rotating for recording, playing or editing an MD.
- 7 Time display

- 8 Group indication
- Lights up when group mode is on.
- 9 Play mode indication Shows play mode of the MD.
- 10 Level meter Shows the volume of the MD being played or recorded.
- 11 REC REMAIN/REMAIN (remaining time/tracks) indication Lights up along with the remaining time of the track, the remaining time of the MD, or the remaining number of tracks.
- 12 Recording mode (LP2/LP4/MONO) indication

10 11

The headphones/earphones with a remote control



- 1 DISPLAY button
- 2 PLAY MODE button
- 3 RPT/ENT (repeat/enter) button
- 4 SOUND button
- 5 Clip
- 6 II (pause) button
- T Control (I≪√/ → →)

 → → : play, AMS, FF

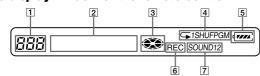
 : REW, AMS

 Turn or turn and hold to play, fast

forward, rewind, etc.

- 8 Control (VOL +/-) Pull and turn to adjust the volume.
- 9 Display window
- 10 HOLD switch
- 11 (stop) button May be used as the "Enter" button, depending on the function.

The display window of the remote control

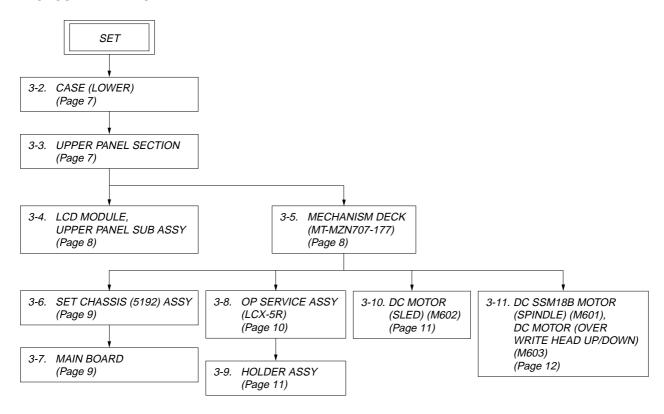


- 1 Track number display
- 2 Character information display
- 3 Disc indication
- 4 Play mode indication
- 5 Battery level indication
- 6 REC indication
- 7 SOUND indication

SECTION 3 DISASSEMBLY

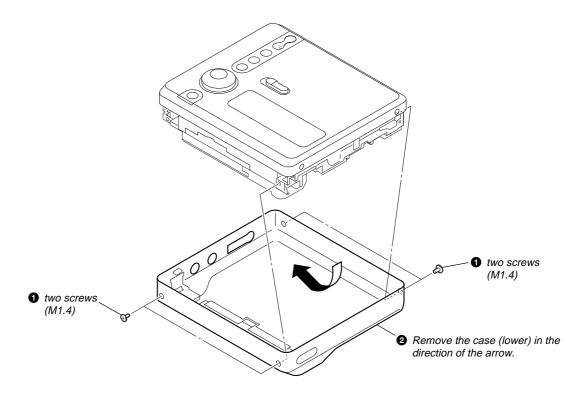
• This set can be disassembled in the order shown below.

3-1. DISASSEMBLY FLOW

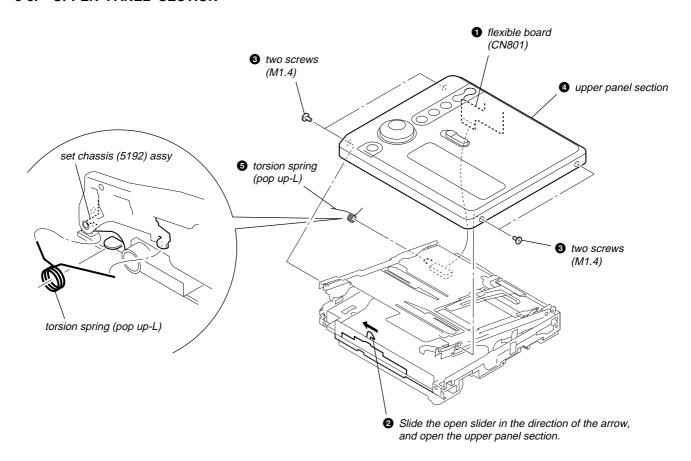


Note: Follow the disassembly procedure in the numerical order given.

3-2. CASE (LOWER)

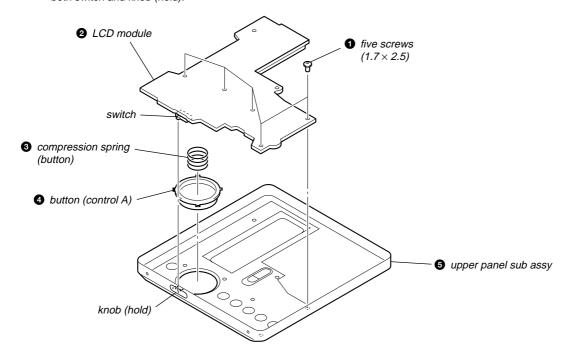


3-3. UPPER PANEL SECTION

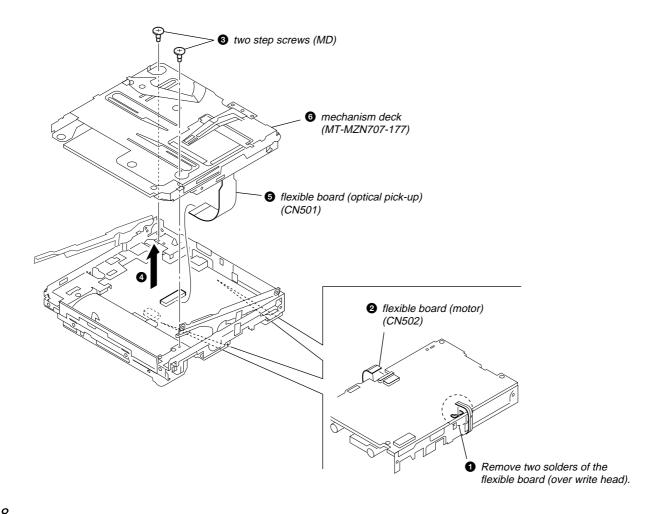


3-4. LCD MODULE, UPPER PANEL SUB ASSY

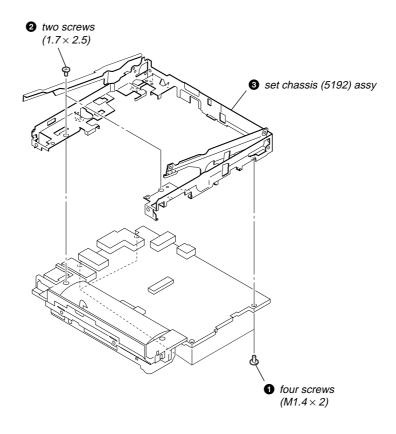
Note: On installation, adjust the position of both switch and knob (hold).



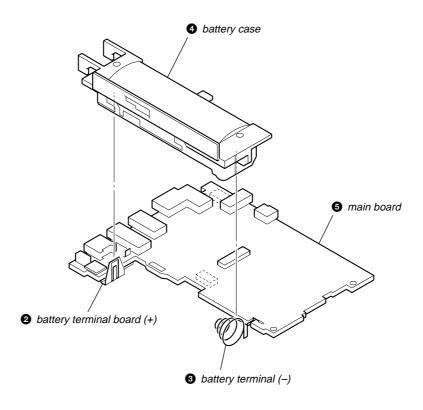
3-5. MECHANISM DECK (MT-MZN707-177)



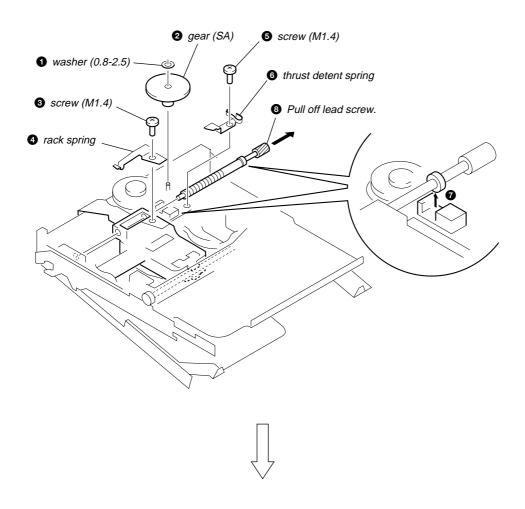
3-6. SET CHASSIS (5192) ASSY

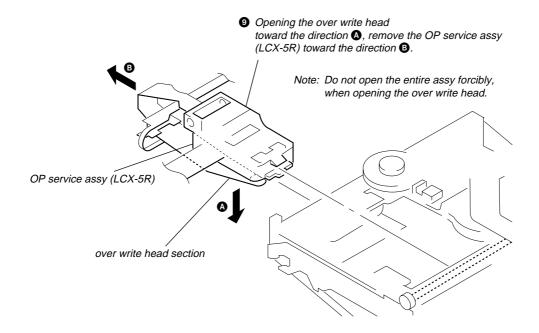


3-7. MAIN BOARD

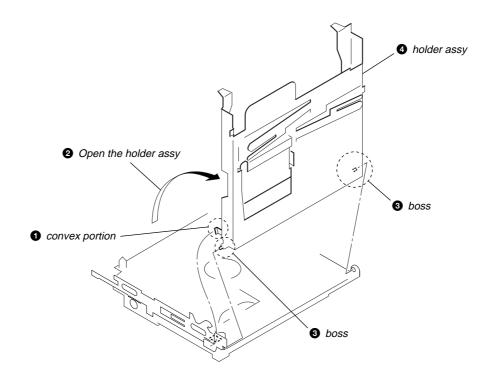


3-8. OP SERVICE ASSY (LCX-5R)

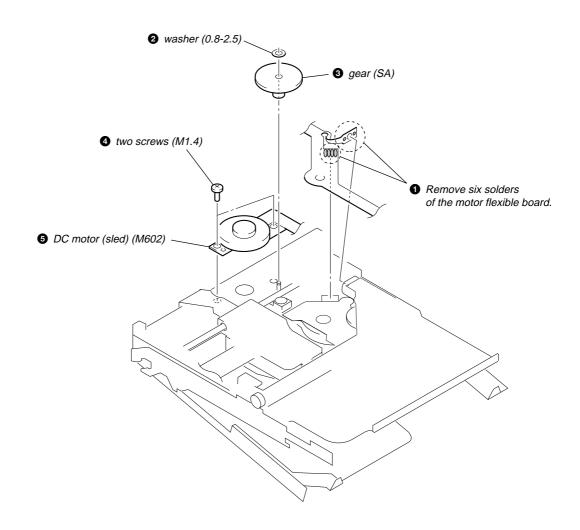




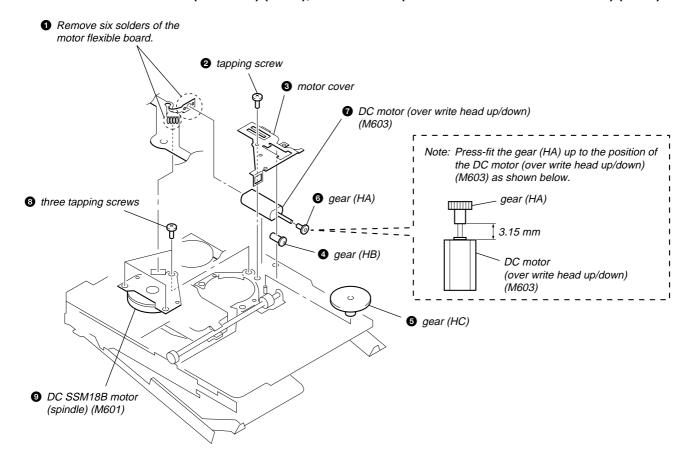
3-9. HOLDER ASSY



3-10. DC MOTOR (SLED) (M602)



3-11. DC SSM18B MOTOR (SPINDLE) (M601), DC MOTOR (OVER WRITE HEAD UP/DOWN) (M603)



SECTION 4 TEST MODE

OUTLINE

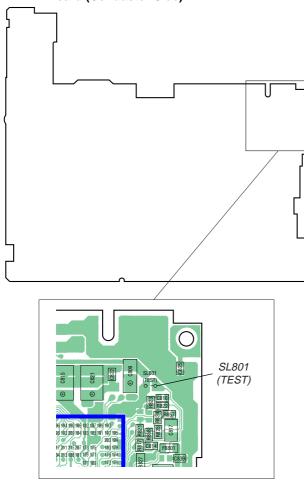
- This set provides the Overall adjustment mode that allows CD and MO discs to be automatically adjusted when in the test mode. In this overall adjustment mode, the disc is discriminate between CD and MO, and each adjustment is automatically executed in order. If a fault is found, the system displays its location. Also, the manual mode allows each individual adjustment to be automatically adjusted.
- Operation in the test mode is performed with the set. A key having no particular description in the text, indicates a set key.

SETTING METHOD OF TEST MODE

There are three different methods to set the test mode:

① Short SL801 (TEST) on the MAIN board with a solder bridge (connect pin (4) of IC801 to the ground). Then, turn on the power.

- MAIN Board (Conductor Side) -



② In the normal mode, turn on the HOLD switch. While pressing the VOL – key press the following order:



③ In the normal mode, turn on the HOLD switch. While pressing the ■/CHG key, press the keys on the remote commander with the following order:

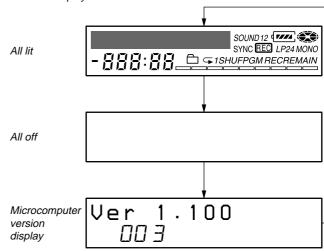


Note: If electrical adjustment (CD and MO overall adjustment) has not been finished completely, "NV Error" is displayed on LCDs of the set and the remote commander.

OPERATION IN SETTING THE TEST MODE

- When the test mode becomes active, first the display check mode is selected.
- Other mode can be selected from the display check mode.
- When the test mode is set, the LCD repeats the following display.

Set LCD display



• When the **II** key is pressed and hold down, the display at that time is held so that display can be checked.

RELEASING THE TEST MODE

For test mode set with the method ①:

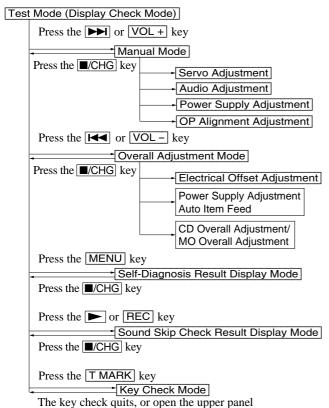
Turn off the power and open the solder bridge on SL801 (TEST) on the MAIN board.

Note: Remove the solders completely. Remaining could be shorted with the chassis, etc.

For test mode set with the method ② or ③:

Turn off the power.

CONFIGURATION OF TEST MODE



MANUAL MODE

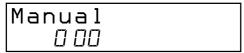
Mode to adjust or check the operation of the set by function. Normally, the adjustment in this mode is not executed. However, the Manual mode is used to clear the memory, power

However, the Manual mode is used to clear the memory, power supply adjustment, and laser power check before performing automatic adjustments in the Overall Adjustment mode.

• Transition Method in Manual Mode

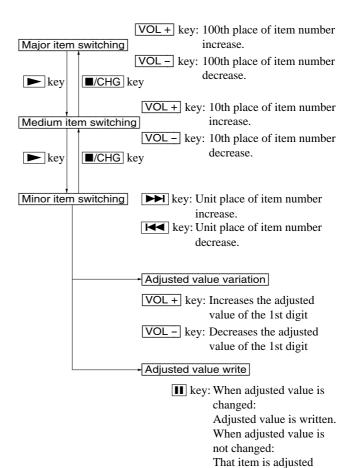
- 1. Set the test mode (see page 13).
- Press the ►►I or VOL+ key activates the manual mode where the LCD display as shown below.

Set LCD display



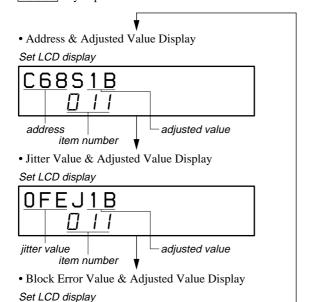
- 3. During each test, the optical pick-up moves outward or inward while the or ward key is pressed for several seconds respectively.
- 4. Each test item is assigned with a 3-digit item number; 100th place is a major item, 10th place is a medium item, and unit place is a minor item.

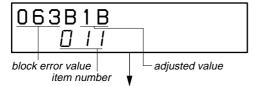
The values adjusted in the test mode are written to the nonvolatile memory (for the items where adjustment was made).



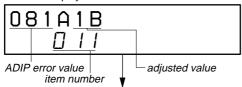
automatically.

5. The display changes a shown below each time the MENU key is pressed.

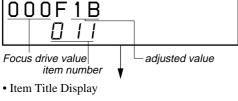




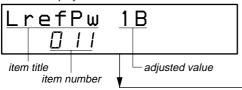
• ADIP Error Value & Adjusted Value Display Set LCD display



• Focus Drive Value & Adjusted Value Display Set LCD display







However in the power mode (item number 700's), only the item is displayed.

6. Quit the manual mode, and press the MCHG key to return to the test mode (display check mode).

OVERALL ADJUSTMENT MODE

Mode to adjust the servo automatically in all items.

Normally, automatic adjustment is executed in this mode at the repair.

For further information, refer to "SECTION 5 ELECTRICAL ADJUSTMENTS" (see page 19).

SELF-DIAGNOSIS RESULT DISPLAY MODE

This set uses the self-diagnostic function system in which if an error occurred during the recording or playing, the mechanism control block and the power supply control block in the microcomputer detect it and record its cause as history in the nonvolatile memory.

By checking this history in the test mode, you can analyze a fault and determine its location.

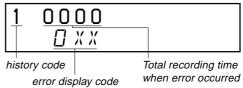
Total recording time is recorded as a guideline of how long the optical pickup has been used, and by comparing it with the total recording time at the time when an error occurred in the self-diagnosis result display mode, you can determine when the error occurred.

Clear both self-diagnosis history data and total recording time, if the optical pickup was replaced.

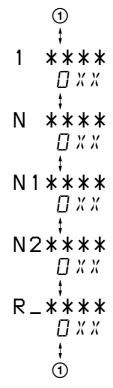
• Self-Diagnosis Result Display Mode Setting Method

- 1. Set the test mode (see page 13).
- 2. In the display check mode, press the MENU key activates the self-diagnosis result display mode where the LCD display as shown below.

Set LCD display



3. Then, each time the key is pressed, LCD display descends by one as shown below. Also, the LCD display ascends by one when the key is pressed.



%% : Error code
**** : Total recording time

If the MENU key is pressed with this display, the LCD switches to the simple display mode.

4. Quit the self-diagnosis result display mode, and press the ☐/ CHG key to return to the test mode (display check mode).

• Description of Error Indication Codes

| Problem | Indication code | Meaning of code | Simple display | Description |
|---------------------------|-----------------|---------------------------------------------|----------------|---------------------------------------------|
| No error | 00 | No error | | No error |
| | 01 | Illegal access target address was specified | Adrs | Attempt to access an abnormal address |
| Servo system error | 02 | High temperature | Temp | High temperature detected |
| | 03 | Focus error | Fcus | Disordered focus or can not read an address |
| | 04 | Spindle error | Spdl | Abnormal rotation of disc |
| TOC error | 11 | TOC error | TOC | Faulty TOC contents |
| TOC error | 12 | Data reading error | Data | Data could not be read at SYNC |
| Power supply system error | 22 | Low battery | LBat | Momentary interruption detected |
| | 31 | Offset error | Ofst | Offset error |
| | 32 | Focus error ABCD offset error | ABCD | Focus error ABCD offset error |
| Offset system error | 33 | Tracking error Offset error | TE | Tracking error Offset error |
| | 34 | X1 tracking error Offset error | X1TE | X1 tracking error Offset error |
| Disc error | 35 | MD DATA 2 disc error | MD2 | MD DATA 2 disc error |

• Description of Indication History

| History code number | Description |
|---------------------|-----------------------------|
| 1 | The first error |
| N | The last error |
| N1 | One error before the last. |
| N2 | Two errors before the last. |
| R_ | Total recording time |

RESET THE ERROR DISPLAY CODE

After servicing, reset the error display code.

• Setting Method of Reset the Error Display Code

- 1. Set the test mode (see page 13).
- 2. Press the MENU key activates the self-diagnosis result display mode.
- 3. To reset the error display code, press the we key (twice) when the code is displayed (except "R_****").

 (All the data on the 1, N, N1, and N2 will be reset)

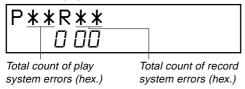
SOUND SKIP CHECK RESULT DISPLAY MODE

This set can display the count of errors that occurred during the recording/playing for checking.

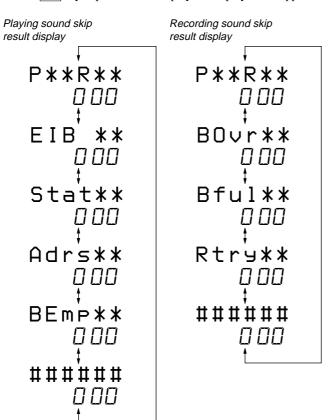
Setting Method of Sound Skip Check Result Display Mode

- 1. Set the test mode (see page 13).
- 2. Press the key or REC key, and the playing or recording sound skip result display mode becomes active respectively where the LCD displays the following.

Set LCD display



3. When the key is pressed, total error count is displayed on the LCD, and each time the key is pressed, the display item moves down by one as shown below. Also, if the key is pressed, the display item moves up by one, then if the REC key is pressed, the display in the record mode appears. When the REC key is pressed, total error count is displayed on the LCD, and each time the key is pressed, the display item moves down by one as shown below. Also, if the key is pressed, the display item moves up by one, then if the key is pressed, the display in the play mode appears.



P**R**: Total play/record errors (hex.)

** : Counter of sound skip check each item (hex.)
#######: 6-digit address where sound was skipped last (hex.)

Cause of Sound Skip Error

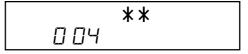
| | Cause of error | Description of error |
|--------|----------------|-------------------------------------------------------------|
| EIB | | Sound error correction error |
| Play | Stat | Decoder status error |
| 1 lay | Adrs | Address access error |
| | BEmp | Buffer is empty |
| BOvr | | Buffer is full, and sounds were dumped |
| Record | Bful | Buffer capacity becomes less, and forcible writing occurred |
| | Rtry | Retry times over |

4. To quit the sound skip check result display mode and to return to the test mode (display check mode), press the □/CHG key.

• Setting Method of Key Check Mode

- 1. Set the test mode (see page 13).
- 2. Press the TMARK key activates the key check mode.

Set LCD display

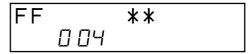


**: AD value of the remote commander key (hexadecimal 00 to FF)

3. When each key on the set and on remote commander is pressed, its name is displayed on the remote commander LCD. (The operated position is displayed for 4 seconds after the slide switch is operated. If any other key is pressed during this display, the remote commander LCD switches to its name display)

Example 1: When the key on the set is pressed:

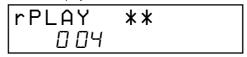
Set LCD display



**: AD value of the remote commander key (hexadecimal 00 to FF)

Example2: When the key on the remote commander is pressed:

Set LCD display



**: AD value of the remote commander key (hexadecimal 00 to FF)

MZ-N707

4. When all the keys on the set and on the remote commander are considered as OK, the following displays are shown for 4 seconds.

Example1: When the keys on the set are considered as OK:

Set LCD display



**: AD value of the remote commander key (hexadecimal 00 to FF)

Example2: When the keys on the remote commander are considered as OK:

Set LCD display



**: AD value of the remote commander key (hexadecimal 00 to FF)

5. When all keys were checked or if the upper panel is opened, the key check mode quits and the test mode (display check mode) comes back.

SECTION 5 **ELECTRICAL ADJUSTMENTS**

OUTLINE

- In this set, automatic adjustment of CD and MO can be performed by entering the test mode.
 - However, before starting automatic adjustment, the memory clear, power supply adjustment, and laser power check must be performed in the manual mode.
- · A key having no particular description in the text, indicates a set key.

PRECAUTIONS FOR ADJUSTMENT

- 1. Adjustment must be done in the test mode only. After adjusting, release the test mode.
- Use the following tools and measuring instruments.

· Test CD disc

TDYS-1

(Part No.: 4-963-646-01)

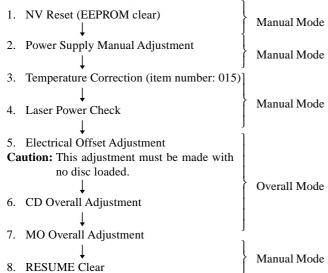
- SONY MO disc available on the market
- · Digital voltmeter
- Laser power meter LPM-8001

(Part No.: J-2501-046-A)

- Thermometer (using the Temperature Correction)
- 3. Unless specified otherwise, supply DC 3V from the DC IN 3V jack (J601).
- 4. Switch position

HOLD switch ON

ADJUSTMENT SEQUENCE



Note: If the version of the microcomputer is 1.000 or later, "3. Temperature Correction" and "2. Power Supply Manual Adjustment" can be performed continuously in reverse order with pressing the **END** SEARCH key in the overall adjustment mode.

NV RESET

Caution: The shipment data will be cleared when the NV is reset. Therefore, change the NV adjusted values following the Change of NV Adjusted Values immediately after the NV was reset.

> Change the NV adjustment values according to the microcomputer version.

. Setting Method of NV Reset

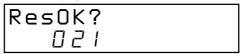
1. Select the manual mode of the test mode, and set item number 021 NV Reset (see page 14).

Set LCD display

ResNU CC 021

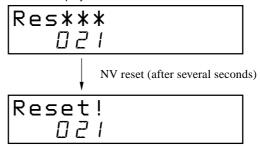
2. Press the **II** key.

Set LCD display



3. Press the **II** key once more.

Set LCD display



4. Press the **■/CHG** key to quit the manual mode, and return the test mode (display check mode).

• Change of NV Adjusted Values (Version 1.100)

Caution: Change the NV adjustment values according to the microcomputer version.

In this set, some adjusted values were set in the manual mode at the shipment, but these will be cleared when the NV is reset. Therefore, modify the NV adjusted values through the following procedure immediately after the NV was reset.

- 1. Item numbers in which the NV adjusted values are to be modi-
 - Change ABCD gain [Hpit] initial value (item number 336) Caution: The ABCD gain [Hpit] initial value must be changed before the overall adjustment.
 - Change BatFlg value (item number 741)
 - Change NiRec0 to 4 values (item numbers 771 to 775)
 - Change NiPb 0 to 4 values (item numbers 776 to 779, 781)
 - Change CLV drive voltage limiter (item numbers 865, 866)
 - Change x2 CLV speed gain, +6dB compared to conventional (item numbers 867, 868)
 - Change x2 CLV phase gain, +3dB compared to conventional (item numbers 871, 872)
 - Change x2/x1 switching temperature threshold value $(+10^{\circ}\text{C} \rightarrow +5^{\circ}\text{C})$ (item numbers 873, 874)

- 2. NV adjusted values modifying procedure
- 1) Select manual mode of the test mode, and set item number 336 (see page 14).

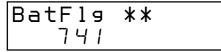
Set LCD display



**: Adjusted value

- Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 08.
- 3) Press the **III** key to write the adjusted value.
- 4) Select manual mode of the test mode, and set item number 741 (see page 14).

Set LCD display



**: Adjusted value

- Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 02.
- 6) Press the **III** key to write the adjusted value.
- 7) Select manual mode of the test mode, and set item number 771 (see page 14).

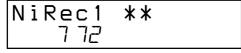
Set LCD display



**: Adjusted value

- Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 6E.
- 9) Press the **II** key to write the adjusted value.
- 10) Select manual mode of the test mode, and set item number 772 (see page 14).

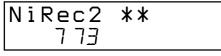
Set LCD display



**: Adjusted value

- 11) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 7C.
- 12) Press the **III** key to write the adjusted value.
- 13) Select manual mode of the test mode, and set item number 773 (see page 14).

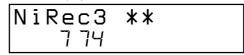
Set LCD display



**: Adjusted value

- 14) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 7D.
- 15) Press the **II** key to write the adjusted value.
- 16) Select manual mode of the test mode, and set item number 774 (see page 14).

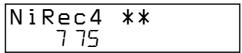
Set LCD display



**: Adjusted value

- 17) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 7F
- 18) Press the **III** key to write the adjusted value.
- 19) Select manual mode of the test mode, and set item number 775 (see page 14).

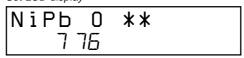
Set LCD display



**: Adjusted value

- 20) Adjust with the VOL + key (adjusted value up) or VOL-key (adjusted value down) so that the adjusted value becomes 81.
- 21) Press the **II** key to write the adjusted value.
- 22) Select manual mode of the test mode, and set item number 776 (see page 14).

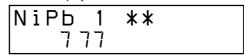
Set LCD display



**: Adjusted value

- 23) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes
- 24) Press the **III** key to write the adjusted value.
- 25) Select manual mode of the test mode, and set item number 777 (see page 14).

Set LCD display

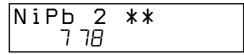


**: Adjusted value

- 26) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 7B.
- 27) Press the **III** key to write the adjusted value.

28) Select manual mode of the test mode, and set item number 778 (see page 14).

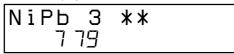
Set LCD display



**: Adjusted value

- 29) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 7D.
- 30) Press the **II** key to write the adjusted value.
- 31) Select manual mode of the test mode, and set item number 779 (see page 14).

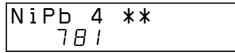
Set LCD display



**: Adjusted value

- 32) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 7F.
- 33) Press the **III** key to write the adjusted value.
- 34) Select manual mode of the test mode, and set item number 781 (see page 14).

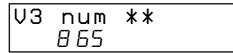
Set LCD display



**: Adjusted value

- 35) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 81.
- 36) Press the **III** key to write the adjusted value.
- 37) Select manual mode of the test mode, and set item number 865 (see page 14).

Set LCD display



**: Adjusted value

- 38) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 2C.
- 39) Press the **III** key to write the adjusted value.
- 40) Select manual mode of the test mode, and set item number 866 (see page 14).

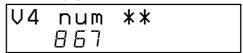
Set LCD display



**: Adjusted value

- 41) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes B9.
- 42) Press the **III** key to write the adjusted value.
- 43) Select manual mode of the test mode, and set item number 867 (see page 14).

Set LCD display



**: Adjusted value

- 44) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes C9.
- 45) Press the **III** key to write the adjusted value.
- 46) Select manual mode of the test mode, and set item number 868 (see page 14).

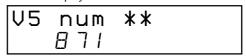
Set LCD display



**: Adjusted value

- 47) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 80
- 48) Press the **II** key to write the adjusted value.
- 49) Select manual mode of the test mode, and set item number 871 (see page 14).

Set LCD display



**: Adjusted value

- 50) Adjust with the VOL + key (adjusted value up) or VOL-key (adjusted value down) so that the adjusted value becomes
- 51) Press the **II** key to write the adjusted value.
- 52) Select manual mode of the test mode, and set item number 872 (see page 14).

Set LCD display

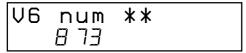


**: Adjusted value

- 53) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 2D.
- 54) Press the **III** key to write the adjusted value.

55) Select manual mode of the test mode, and set item number 873 (see page 14).

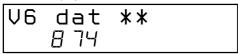
Set LCD display



**: Adjusted value

- 56) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes BB.
- 57) Press the **II** key to write the adjusted value.
- 58) Select manual mode of the test mode, and set item number 874 (see page 14).

Set LCD display



**: Adjusted value

- 59) Adjust with the VOL + key (adjusted value up) or VOL key (adjusted value down) so that the adjusted value becomes 05
- 60) Press the **III** key to write the adjusted value.

POWER SUPPLY MANUAL ADJUSTMENT

· Adjustment Sequence

Adjustment must be done with the following steps.

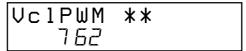
- 1. Vc PWM Duty (L) adjustment (item number: 762)
- 2. Vc PWM Duty (H) adjustment (item number: 763)
- 3. VI PWM Duty (L) adjustment (item number: 764)
- 4. VI PWM Duty (H) adjustment (item number: 765)
- ↓
 5. Vrec PWM Duty (L) adjustment (item number: 766)
- ↓6. Vrec PWM Duty (H) adjustment (item number: 767)

• Setting Method of Power Supply Manual Adjustment

- 1. Make sure that the power supply voltage is 3V.
- 2. Select the manual mode of the test mode (see page 14).
- Set item number.

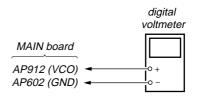
 Adjustment Method of Vc PWM Duty (L) (item number: 762)

Set LCD display



**: Adjusted value

 Connect a digital voltmeter to the AP912 (VCO) on the MAIN board, and adjust VOL + key (voltage up) or VOL - key (voltage down) so that the voltage becomes 2.40 ± 0.05 V.



2. Press the wite the adjusted value.

Adjustment and Connection Location: MAIN board (see page 25)

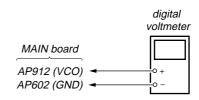
 Adjustment Method of Vc PWM Duty (H) (item number: 763)

Set LCD display



**: Adjusted value

Connect a digital voltmeter to the AP912 (VCO) on the MAIN board, and adjust VOL + key (voltage up) or VOL - key (voltage down) so that the voltage becomes 2.75 ± 0.05 V.

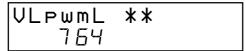


2. Press the **III** key to write the adjusted value.

Adjustment and Connection Location: MAIN board (see page 25)

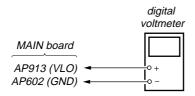
 Adjustment Method of VI PWM Duty (L) (item number: 764)

Set LCD display



**: Adjusted value

 Connect a digital voltmeter to the AP913 (VLO) on the MAIN board, and adjust VOL + key (voltage up) or VOL - key (voltage down) so that the voltage becomes 2.30 ± 0.01 V.

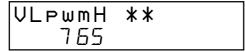


2. Press the **II** key to write the adjusted value.

Adjustment and Connection Location: MAIN board (see page 25)

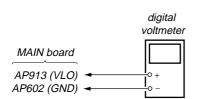
 Adjustment Method of VI PWM Duty (H) (item number: 765)

Set LCD display



**: Adjusted value

1. Connect a digital voltmeter to the AP913 (VLO) on the MAIN board, and adjust VOL + key (voltage up) or VOL - key (voltage down) so that the voltage becomes 2.55 ± 0.01 V.

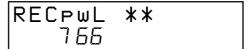


2. Press the **II** key to write the adjusted value.

Adjustment and Connection Location: MAIN board (see page 25)

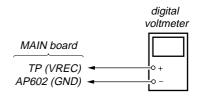
 Adjustment Method of Vrec PWM Duty (L) (item number: 766)

Set LCD display



**: Adjusted value

 Connect a digital voltmeter to the TP (VREC) on the MAIN board, and adjust VOL + key (voltage up) or VOL - key (voltage down) so that the voltage becomes 1.13 ± 0.02 V.

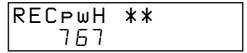


2. Press the wite the adjusted value.

Adjustment and Connection Location: MAIN board (see page 25)

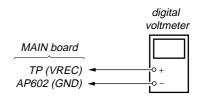
 Adjustment Method of Vrec PWM Duty (H) (item number: 767)

Set LCD display



**: Adjusted value

1. Connect a digital voltmeter to the TP (VREC) on the MAIN board, and adjust VOL + key (voltage up) or VOL - key (voltage down) so that the voltage becomes 1.65 to 1.75 V.



2. Press the **III** key to write the adjusted value.

Adjustment and Connection Location: MAIN board (see page 25)

TEMPERATURE CORRECTION

Adjustment Method of Temperature Correction

1. Select the manual mode of test mode, and set the item number 015 (see page 14).

Set LCD display

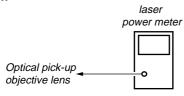


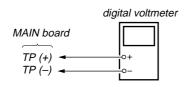
**: Adjusted value

- 2. Measure the ambient temperature.
- 3. Adjust with VOL+, VOL- key so that the adjusted value (hexadecimal value) becomes the ambient temperature. (Initial value: 19h = 25 °C, Adjusting range: 80h to 7fh (-128 °C to +127 °C)
- 4. Press the **II** key to write the adjusted value.

LASER POWER CHECK

Connection





Checking Method

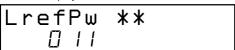
1. Select the manual mode of test mode (see page 14), and set the laser power adjusting mode (item number 010).

Set LCD display



- Press the key continuously until the optical pick-up moves to the most inward track.
- 3. Open the cover and set the laser power meter on the objective lens of the optical pick-up.
- 4. Press the key, and set the laser MO read adjustment mode (item number 011).

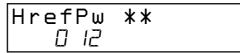
Set LCD display



- 5. Check that the laser power meter reading is 0.81 ± 0.08 mW.
- 6. Check that the voltage both ends (TP (+) and TP (-)) of resistor R521 at this time is below 44 mV.

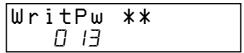
7. Press the key, and set the laser CD read adjustment mode (item number 012).

Set LCD display



- 8. Check that the laser power meter reading is 0.97 ± 0.10 mW.
- 9. Check that the voltage both ends (TP (+) and TP (-)) of resistor R521 at this time is below 44 mV.
- Press the key, and set the laser MO write adjustment mode (item number 013).

Set LCD display

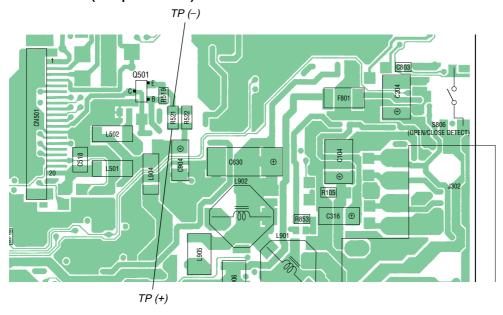


- 11. Check that the laser power meter reading is 4.95 ± 0.50 mW.
- 12. Check that the voltage both ends (TP (+) and TP (-)) of resistor R521 at this time is below 80 mV.
- 13. Press the **■/CHG** key to quit the manual mode, and activate the test mode (display check mode).

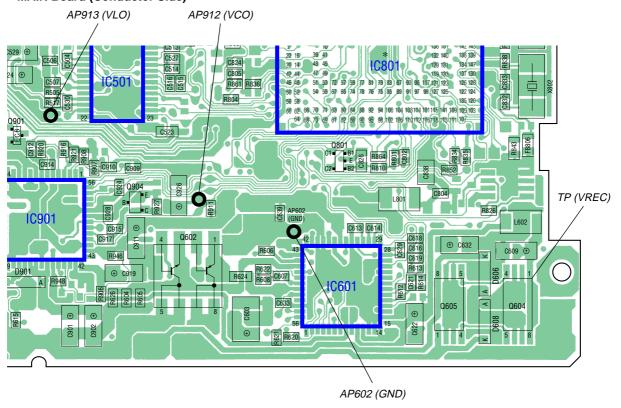
Checking and Connection Location: MAIN board (see page 25)

Adjustment/checking and Connection Location:

- MAIN Board (Component Side) -

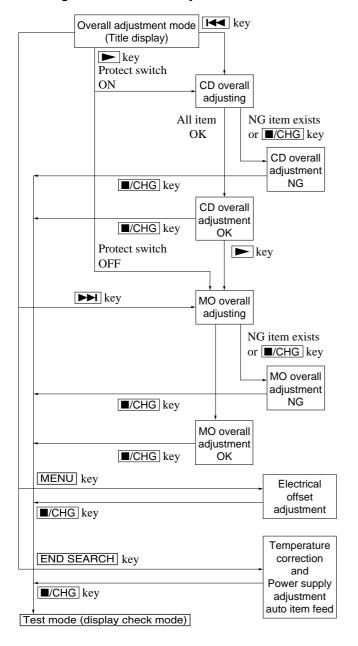






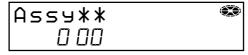
OVERALL ADJUSTMENT MODE

• Configuration of Overall Adjustment Mode



• Overall Adjustment Mode (Title Display)

Set LCD display



: (Disc mark) At end of power supply adjustment: Outside lit
At end of electrical offset adj.: Inside lit

**: Left side = MO overall adjustment information

F*: MO overall adjustment completed

1*: Manual adjustment exists (overall adj. not completed)

0*: Not adjusted

Rifht side = CD overall adjustment information

*F: CD overall adjustment completed

*1: Manual adjustment exists (overall adj. not completed)

*0: Not adjusted

Note: Adjust the CD first, when performing adjustment.

• Electrical Offset Adjusting Method

Caution: The electrical offset adjustment must be made with no disc loaded.

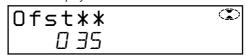
- 1. Make sure that the power supply voltage is 3 V.
- 2. Set the test mode (see page 13).

Set LCD display



4. Press the MENU key.

Set LCD display



Electrical offset adjustment is over, if the following display appears.

Set LCD display



- Adjustment Method of CD and MO Overall Adjustment Mode
- 1. Set the test mode (see page 13).

Set LCD display



3. Insert CD disc in the set, and press thethe ► key to set the CD overall adjustment mode. Automatic adjustments are made.

Set LCD display



XXX: Item number for which an adjustment is being executed.

4. In case of CD overall adjustment NG, reset the NV (see page 19), then readjust from the temperature correction (see page 24).

Set LCD display



**: NG item number.

5. If OK through the CD overall adjustments, then perform MO overall adjustments.

Set LCD display



 Insert MO disc in the set, and press the key to set the MO overall adjustment mode. Automatic adjustments are made.

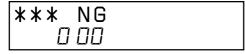
Set LCD display



XXX: Item number for which an adjustment is being executed.

7. In case of MO overall adjustment NG, reset the NV (see page 19), then readjust from the temperature correction (see page 24).

Set LCD display



**: NG item number.

8. If OK through the MO overall adjustments, press the THG key to return to the test mode and terminate the overall adjustment mode.

Set LCD display

| MO | OK |
|----|------|
| | 0 00 |

• CD and MO Overall Adjustment Items

1. CD overall adjustment items

| Item No. | Description | |
|----------|--------------------------------------|--|
| 312 | | |
| 313 | CD electrical offset adjustment | |
| 314 | | |
| 321 | CD tracking error gain adjustment | |
| 328 | CD TWPP gain adjustment | |
| 324 | CD tracking array affect adjustment | |
| 332 | CD tracking error offset adjustment | |
| 336 | CD ABCD gain adjustment | |
| 344 | CD focus gain adjustment | |
| 345 | CD tracking gain adjustment | |
| 521 | CD two-axis sensitivity adjustment | |
| 522 | CD two-axis sensitivity adjustifient | |

2. MO overall adjustment items

| 2. MO overall adjustment items | | |
|--------------------------------|----------------------------------------------------|--|
| Item No. | Description | |
| 112 | | |
| 113 | MO electrical offset adjustment | |
| 114 | | |
| 118 | | |
| 221 | Low reflective CD tracking error gain adjustment | |
| 224 | Low reflective CD tracking error offset adjustment | |
| 232 | Lett remodate ob tracking error error aujustment | |
| 236 | Low reflective CD ABCD gain adjustment | |
| 244 | Low reflective CD focus gain adjustment | |
| 245 | Low reflective CD tracking gain adjustment | |
| 121 | MO tracking error gain adjustment | |
| 122 | MO TON offset adjustment | |
| 134 | MO TWPP gain adjustment | |
| 131 | MO triple speed read TWPP offset adjustment | |
| 132 | | |
| 136 | MO ABCD gain adjustment | |
| 144 | MO focus gain adjustment | |
| 145 | MO tracking gain adjustment | |
| 138 | MO RF gain adjustment | |
| 434 | MO write TWPP gain adjustment | |
| 431 | MO write TWPP offset adjustment | |
| 432 | MO tracking error offset adjustment | |
| 436 | MO write ABCD gain adjustment | |
| 445 | MO write tracking gain adjustment | |
| 411 | MO normal speed read TWPP offset adjustment | |
| 412 | MO tracking error offset adjustment | |
| 448 | 20 sec full recording | |

MZ-N707

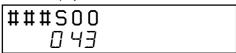
RESUME CLEAR

Perform the Resume clear when all adjustments completed.

• Resume Clear Setting Method

1. Select the manual mode of the test mode, and set item number 043 (see page 14).

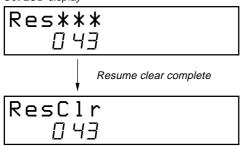
Set LCD display



###: Address

2. Press the **III** key.

Set LCD display



3. Press the **■/CHG** key to return to the test mode (display check mode).

REWRITING THE PATCH DATA AT REPLACEMENT OF MAIN BOARD OR NONVOLATILE MEMORY (IC804)

This set requires the patch data in the nonvolatile memory (IC804) to be rewritten using the application, when the MAIN board or nonvolatile memory (IC804) was replaced.

Caution: The application that meets the microcomputer version in this set must be used when rewriting the patch data. Rewriting the patch data using the application not suitable for the microcomputer version could cause the set to malfunction. For a checking method of the microcomputer version, see "SECTION 4 TEST MODE" (page 13).

Preparation

- 1. USB cable (attached to the set)
- 2. Personal computer in which the Net MD Driver has been installed. (For further information, see "System requirements" (page 4) in "SECTION 1 SERVICING NOTES")
- 3. Application "PatchWriter" for patch data rewriting

. How to Get the Application "PatchWriter" for Patch Data Rewriting

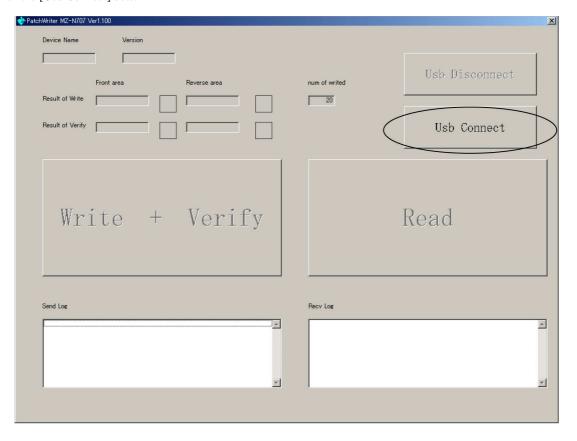
Contact our service technical support division to get the application.

Pre-check

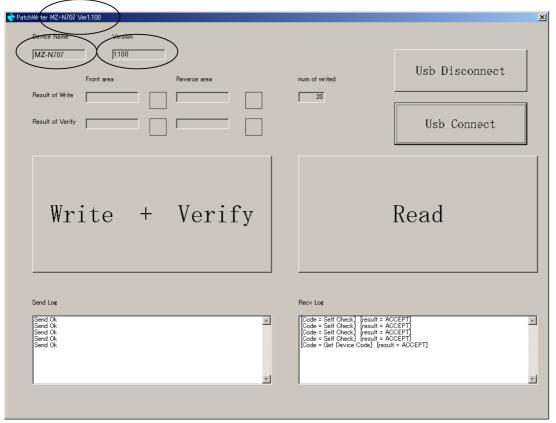
- 1. Check the microcomputer version in this set. (For a checking method of the microcomputer version, see "SECTION 4 TEST MODE" (page 13).)
- 2. Check that the Net MD Driver has been installed in the personal computer.
- Make sure that the set is in the Normal mode.Note: Do not rewrite the patch data in the Test mode.

· Rewriting the Patch Data

- 1. Connect the set to the personal computer with the USB cable.
- 2. Start the application "PatchWriter".
- 3. Make sure that the following window opens.
- 4. Click the [Usb Connect] button.

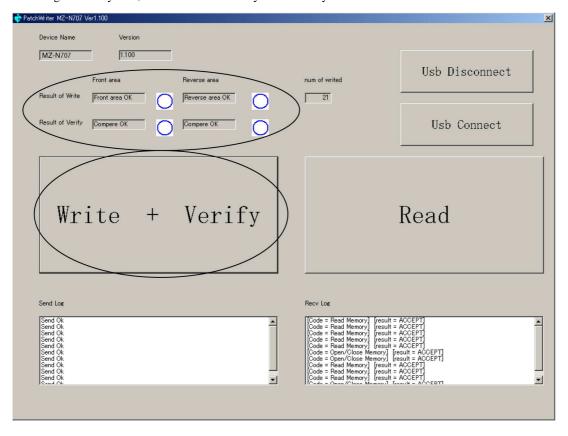


5. Confirm that the model and version indicated on the title bar coincide with the codes displayed in the Device Name block and the Version block in the window.

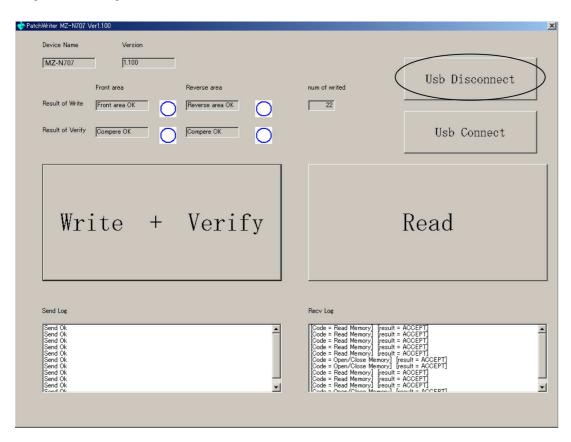


- 6. Click the [Write + Verify] button.
 - The patch data writing and the verify processing will be executed automatically in the following order:
 - 1) Writing to patch area (front area)
 - 2) Writing to patch area (reverse area)
 - 3) Verifying patch area (front area)
 - 4) Verifying patch area (reverse area)
- 7. The operation will terminate with the O mark given to all areas.

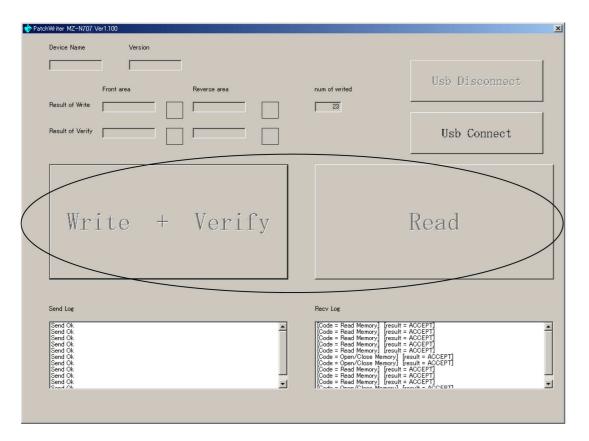
If the × mark is given to any area, the nonvolatile memory will be faulty.



8. Click the [Usb Disconnect] button.



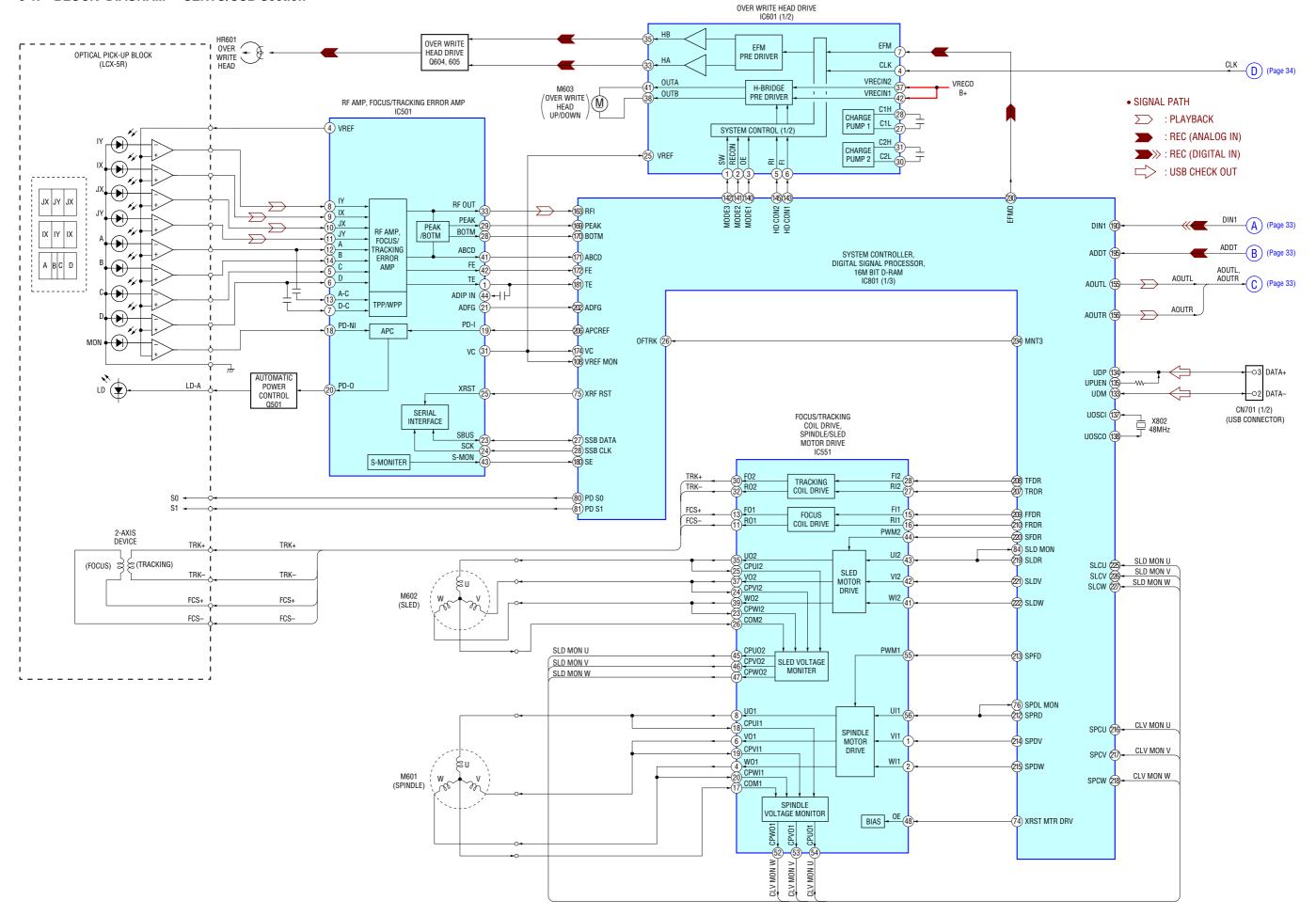
9. Confirm that the window becomes as shown below where the [Write + Verify] button and [Read] button are inactive.



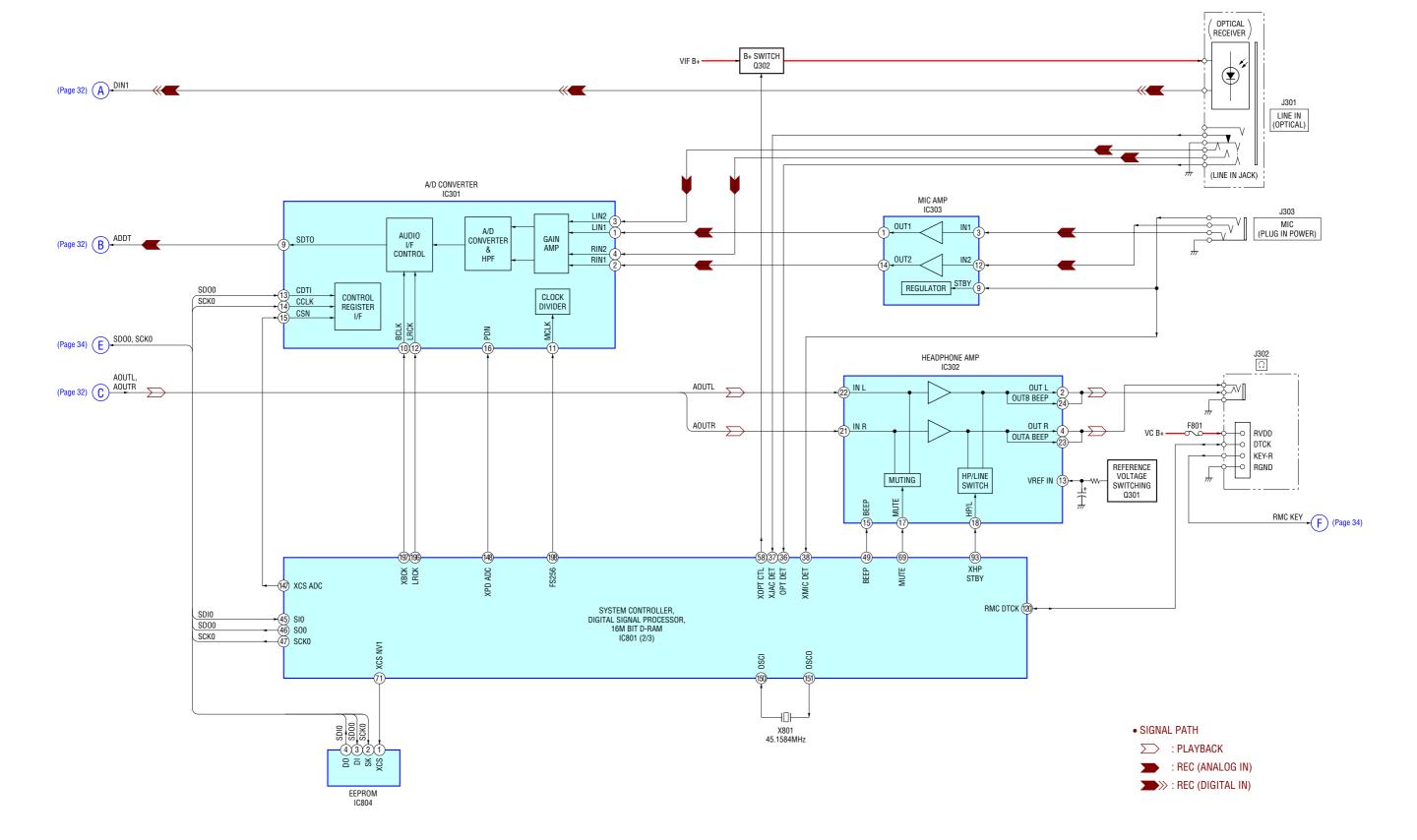
10. Disconnect the USB cable from the personal computer and the set.

SECTION 6 DIAGRAMS

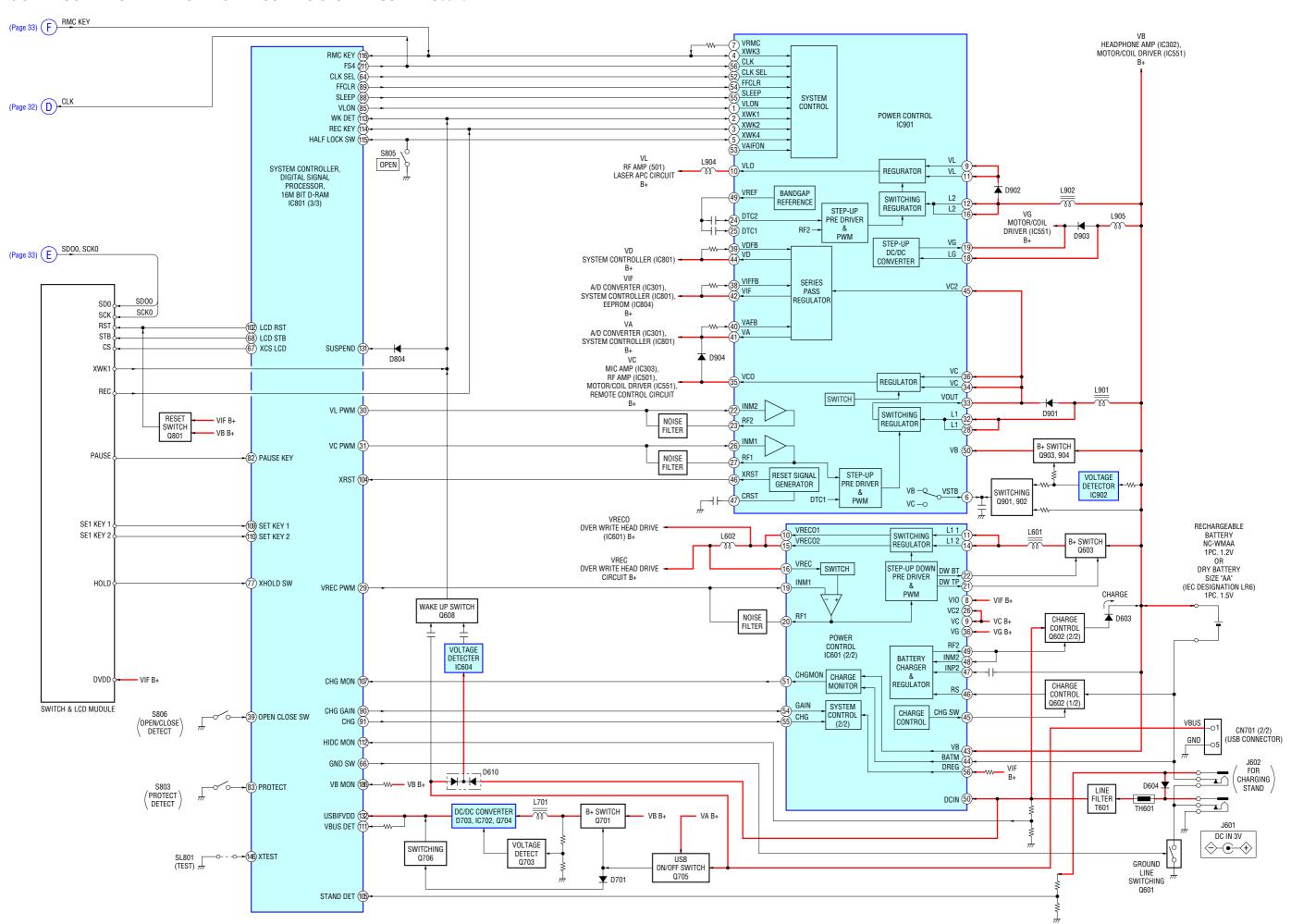
6-1. BLOCK DIAGRAM - SERVO/USB Section -



6-2. BLOCK DIAGRAM - AUDIO Section -



6-3. BLOCK DIAGRAM - DISPLAY/KEY CONTROL/POWER SUPPLY Section -



6-4. NOTE FOR PRINTED WIRING BOARD AND SCHEMATIC DIAGRAMS

Note on Printed Wiring Board:

- • : parts extracted from the component side.
- —— : parts extracted from the conductor side.
- Pattern from the side which enables seeing. (The other layers' patterns are not indicated.)

Caution:

Pattern face side: Parts on the pattern face side seen from (Conductor Side) the pattern face are indicated. Parts face side: Parts on the parts face side seen from (Component Side) the parts face are indicated.

• MAIN board is four-layer printed board. However, the patterns of layers 2 and 3 have not been included in this diagrams.

* Replacement of IC801 used in this set requires a special tool.

· Lead Layouts





Lead layout of conventional IC

CSP (chip size package)

Note on Schematic Diagram:

- All capacitors are in μF unless otherwise noted. pF: $\mu \mu F$ 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and $^{1}\!/_{4}\,W$ or less unless otherwise specified.
- % : indicates tolerance.
- _____ : panel designation.

with mark \triangle are critical for safety. Replace only with part number specified.

- : B+ Line.
- Total current is measured with MD installed.
- Power voltage is dc 3 V and fed with regulated dc power supply from DC IN 3 V jack (J601).
- Voltages and waveforms are dc with respect to ground in playback mode.

no mark : PLAYBACK

) : REC

⟩⟩ : USB

* : Impossible to measure

- Voltages are taken with a VOM (Input impedance $10 \text{ M}\Omega$). Voltage variations may be noted due to normal production tolerances.
- · Waveforms are taken with a oscilloscope. Voltage variations may be noted due to normal production tolerances.
- Circled numbers refer to waveforms.
- · Signal path.

 \sum : PLAYBACK

: REC (ANALOG IN) : REC (DIGITAL IN) : USB CHECK OUT

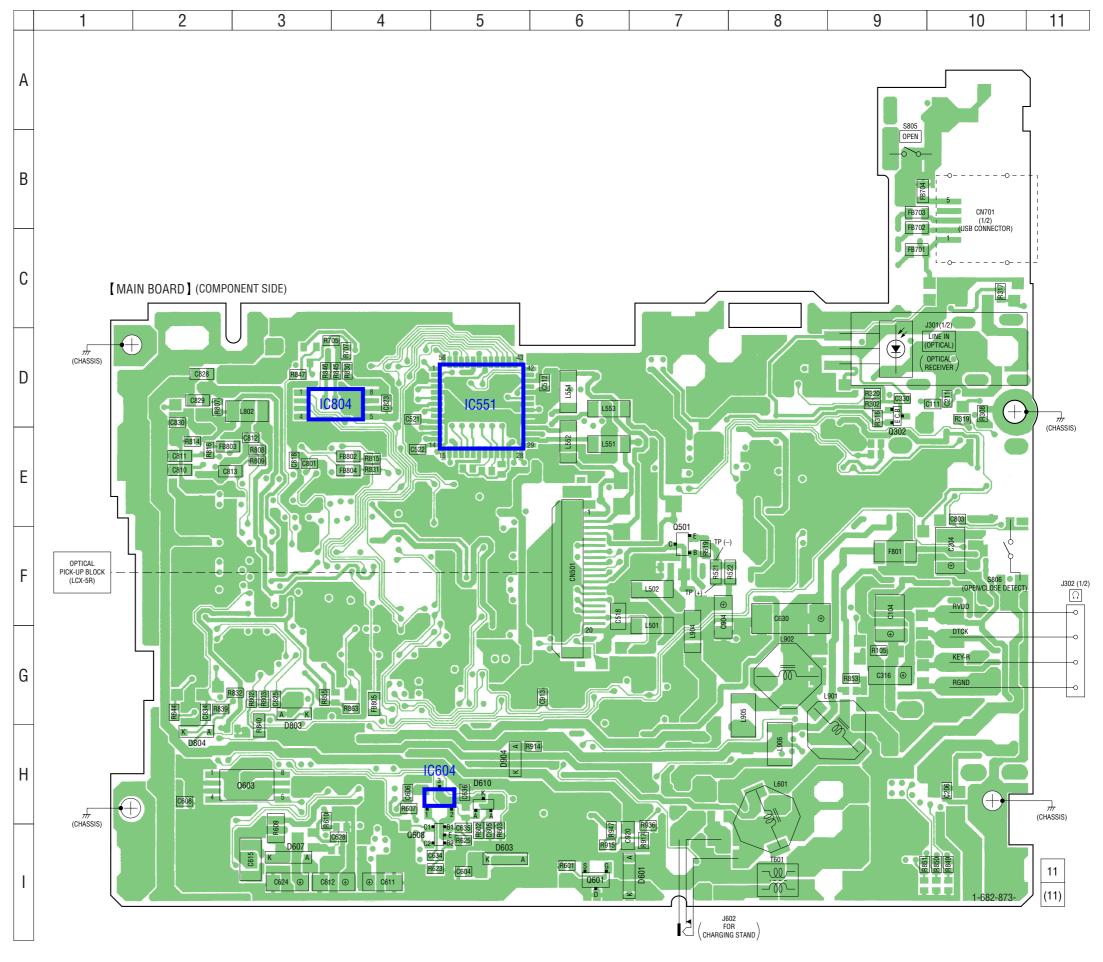
- * Replacement of IC801 used in this set requires a special tool.
- The voltage and waveform of CSP (chip size package) cannot be measured, because its lead layout is different form that of conventional IC.
- Abbreviation

FR : French model

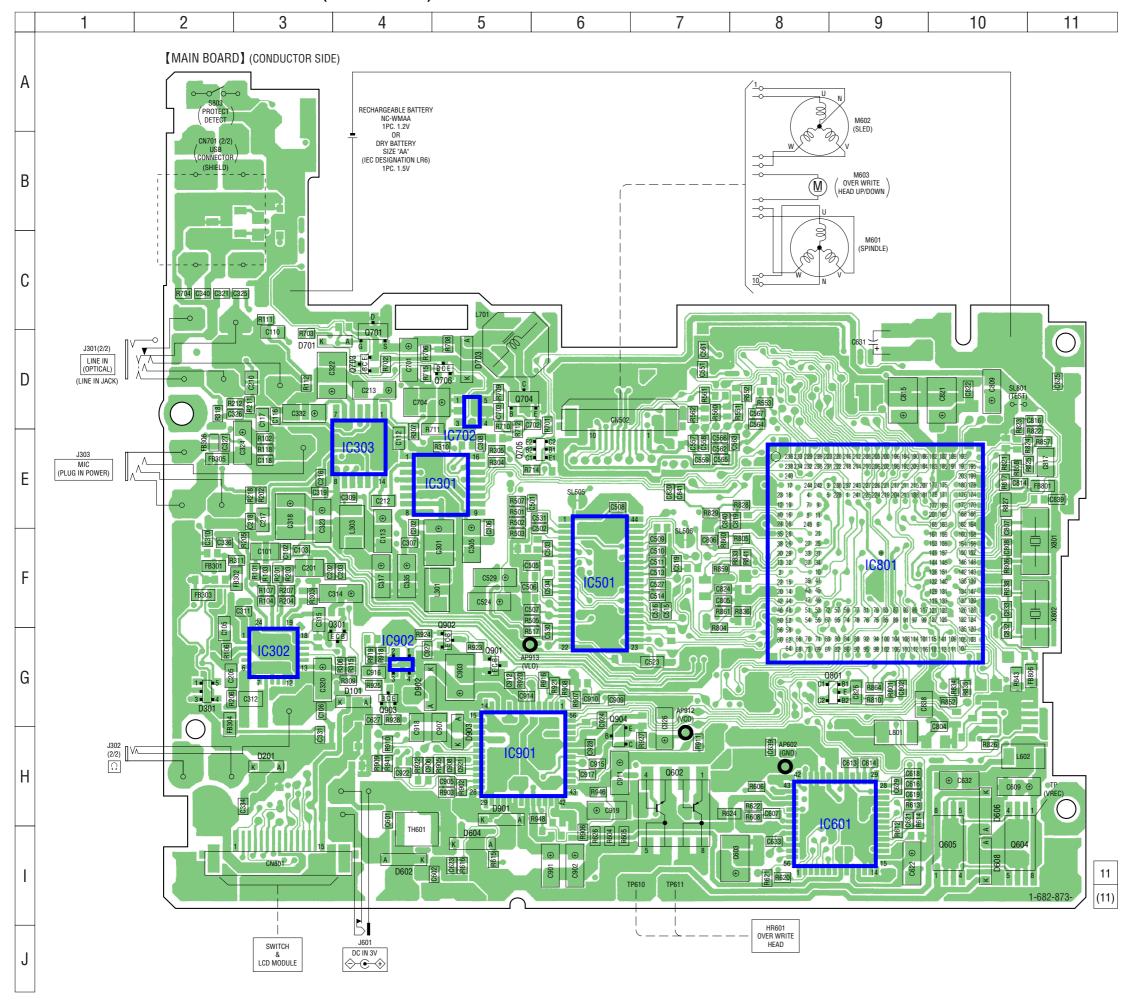
• Semiconductor Location

| Ref. No. | Location |
|----------|----------|
| D601 | I-7 |
| D603 | I-5 |
| D607 | I-3 |
| D610 | H-5 |
| D803 | G-3 |
| D804 | H-2 |
| D904 | H-5 |
| IC551 | D-5 |
| IC604 | H-5 |
| IC804 | D-4 |
| Q302 | D-9 |
| Q501 | F-7 |
| Q601 | I-6 |
| Q603 | H-3 |
| Q608 | I-5 |

6-5. PRINTED WIRING BOARD - MAIN Board (Component Side) - ** :Uses unleaded solder.



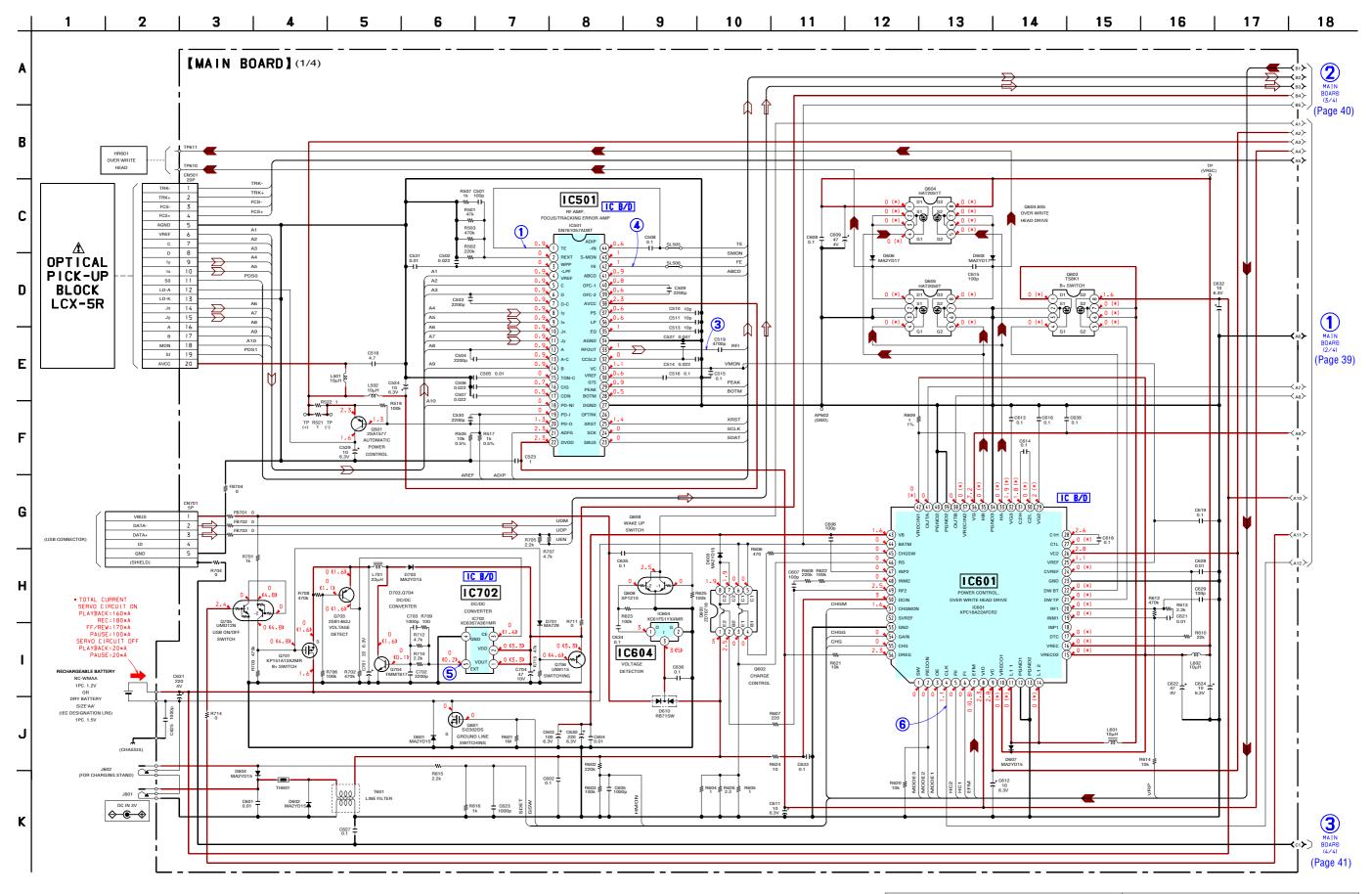
6-6. PRINTED WIRING BOARD - MAIN Board (Conductor Side) - ** :Uses unleaded solder.



Semiconductor Location

| Location | | | | | |
|--------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------|--|--|--|--|
| Ref. No. | Location | | | | |
| D101 D201 D301 D602 D604 D606 D608 D701 D703 D901 D902 D903 | G-4 H-3 G-2 I-4 I-5 H-10 I-10 D-4 D-5 H-5 G-4 H-5 | | | | |
| IC301 IC302 IC303 IC501 IC601 IC702 IC801 IC901 IC902 | E-5 G-3 E-4 F-6 H-9 D-5 F-9 H-5 G-4 | | | | |
| Q301 Q602 Q604 Q605 Q701 Q703 Q704 Q705 Q706 Q801 Q901 Q902 Q903 Q904 | G-4 H-7 I-10 I-10 D-4 D-5 E-6 D-5 G-9 G-5 G-5 G-4 H-6 | | | | |

6-7. SCHEMATIC DIAGRAM - MAIN Board (1/4) - • See page 42 for Waveforms. • See page 43 for IC Block Diagrams.

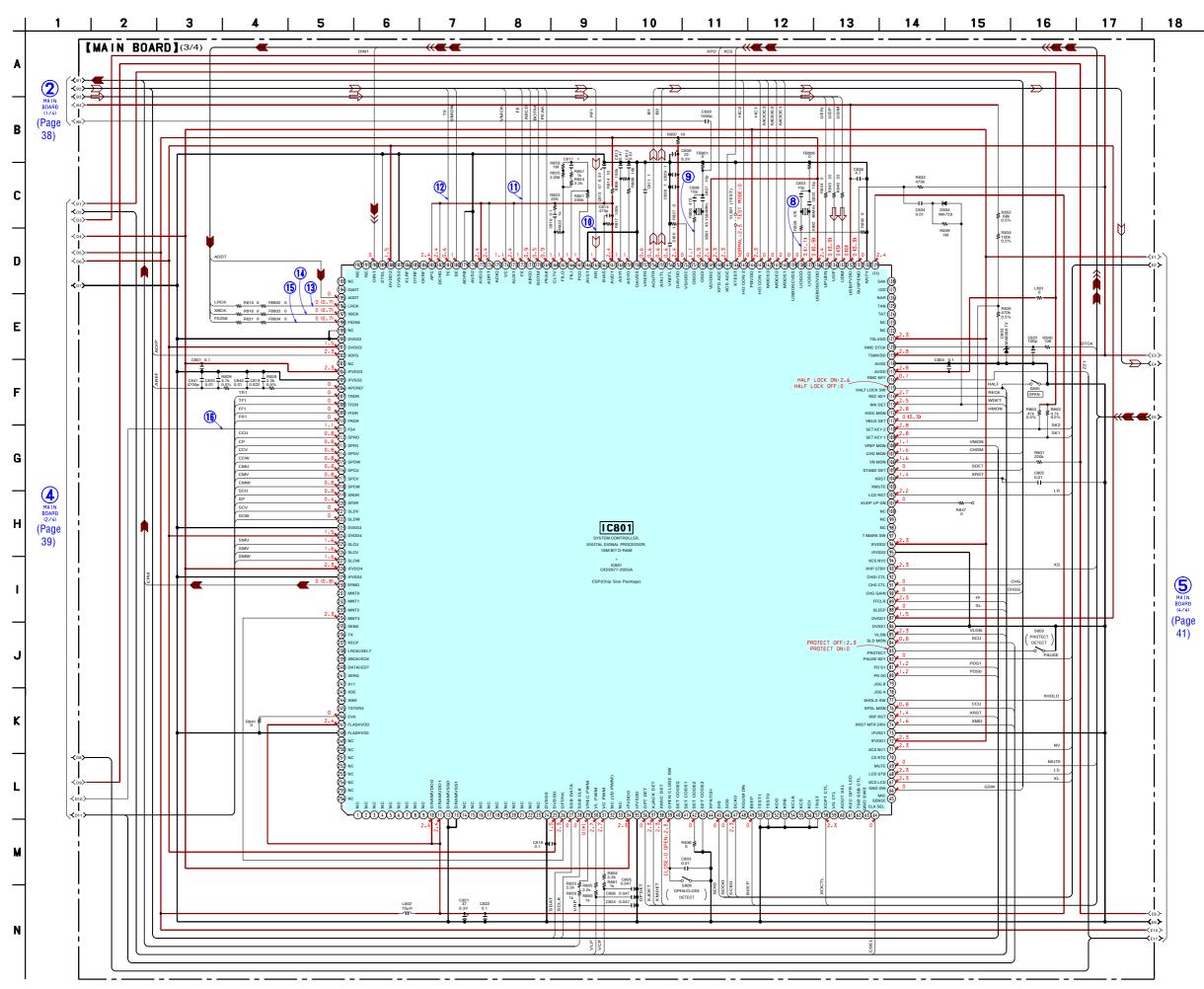


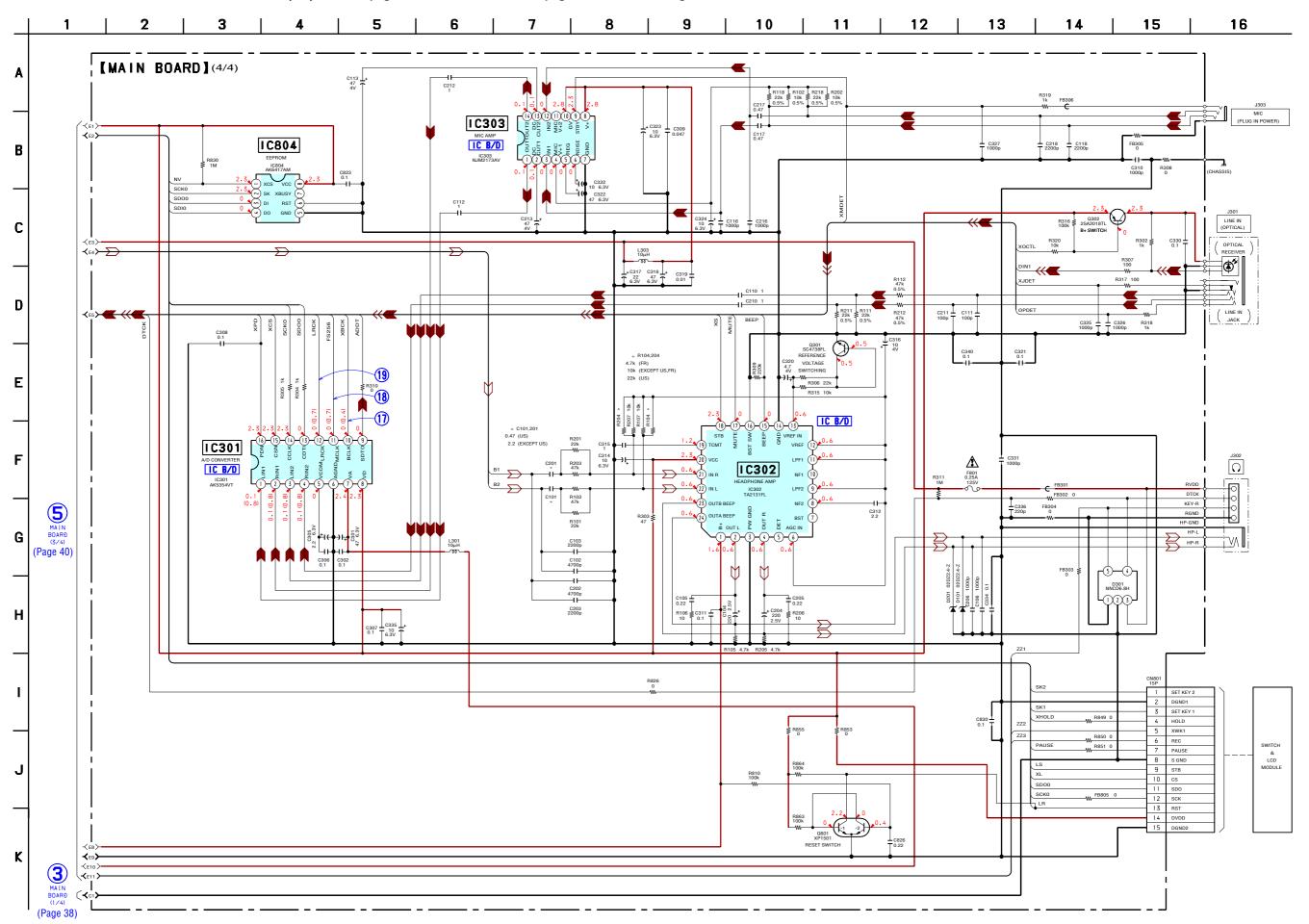
The components identified by mark \triangle or dotted line with mark \triangle are critical for safety. Replace only with part number specified.

Les composants identifiés par une marque riangle sont critiques pour la sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

6-8. SCHEMATIC DIAGRAM - MAIN Board (2/4) - • See page 42 for Waveform. • See page 43 for IC Block Diagrams. 2 3 5 6 7 | 8 | 9 | 10 11 12 13 14 | 15 16 17 [MAIN BOARD] (2/4) ·6 IC B/D R910 **≢** 10k R909 10k C920 10 6.3V MAIN BOARD (1/4) (Page 38) D R941 22k I C551 IC B/D MAIN BOARD (3/4) (Page 40) C917 0.1 IC901 G C924 1000p L905 100μH ₹R922 22k VOLTAGE DETECTOR IC902 XCG1CN0802NR Н SLEDU SLEDV SLEDW HD DRV-L902 22μH HD DRV+ CLVW CLVV CLVU CLVN C562 0.022 C565 0.01 C563 0.022 C566 0.01 C564 0.022 C567 0.01

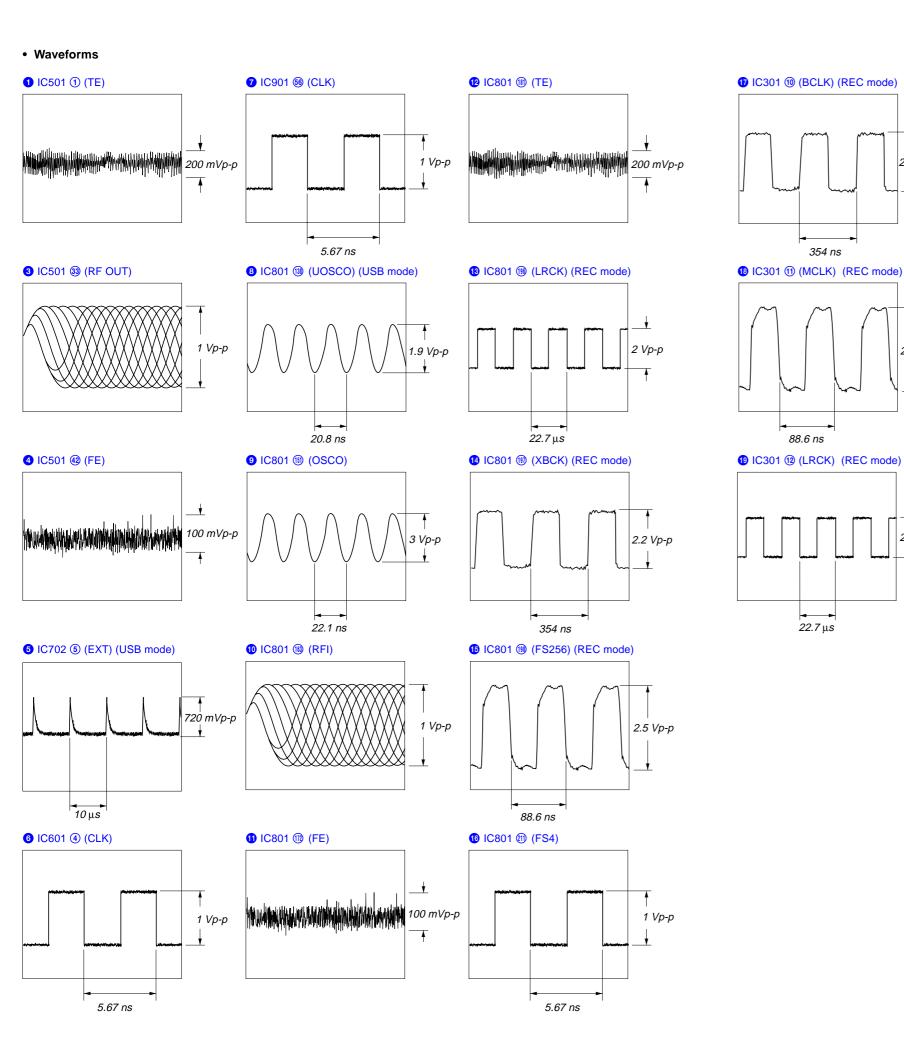
6-9. SCHEMATIC DIAGRAM - MAIN Board (3/4) - • See page 42 for Waveforms.





41

MZ-N707



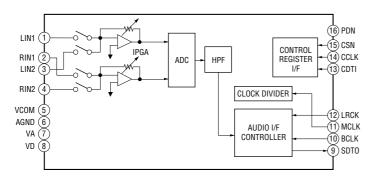
2.2 Vp-p

2.5 Vp-p

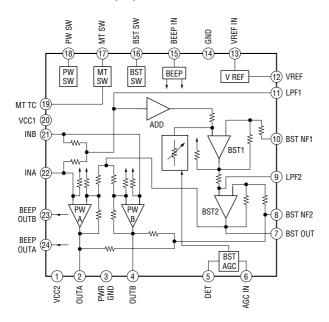
2 Vp-p

• IC Block Diagrams

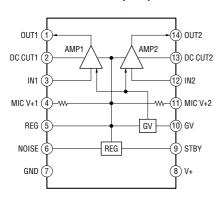
IC301 AK5354VT-E2



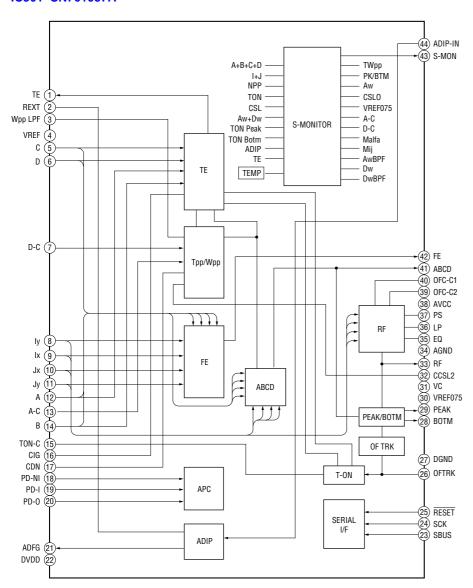
IC302 TA2131FL (EL)



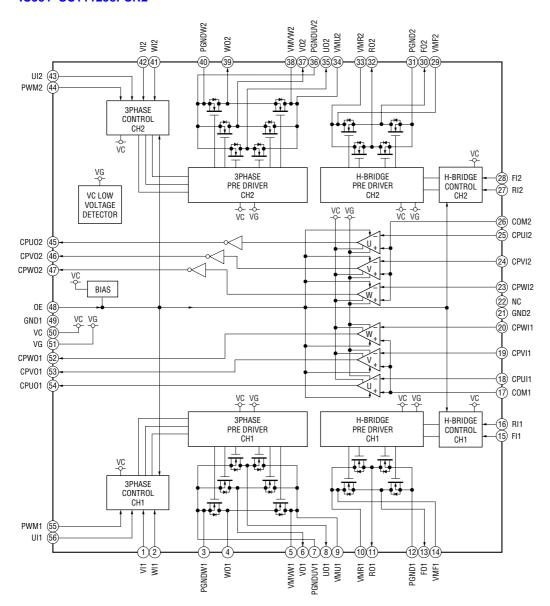
IC303 NJM2173AV (TE2)



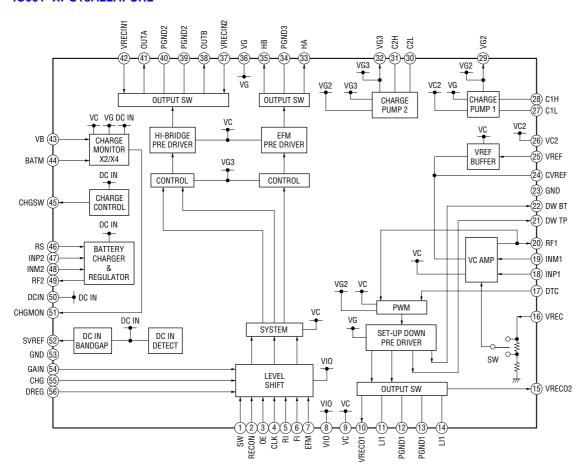
IC501 SN761057A



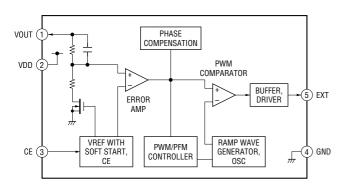
IC551 SC111258FCR2



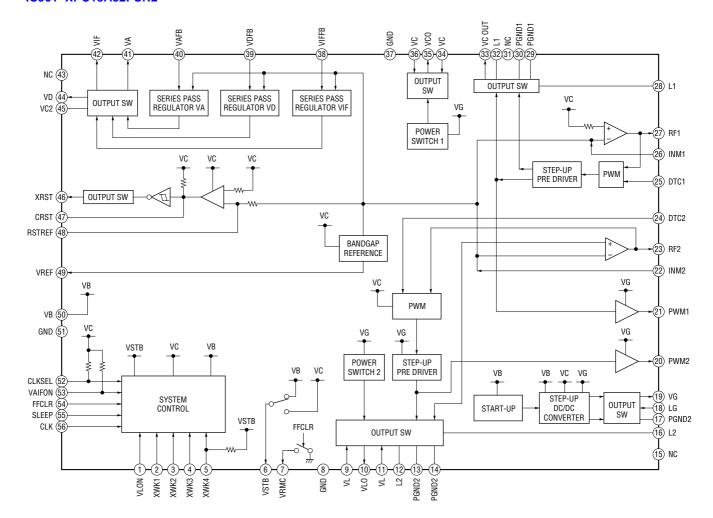
IC601 XPC18A22AFCR2



IC702 XC6367A361MR



IC901 XPC18A32FCR2



6-11. IC PIN FUNCTION DESCRIPTION

• IC501 SN761057A (RF AMP, FOCUS/TRACKING ERROR AMP)

| Pin No. | Pin Name | I/O | Description |
|----------|--------------|-----|-----------------------------------------------------------------------------------------------|
| 1 | TE | О | Tracking error signal output to the system controller |
| 2 | REXT | _ | Connect terminal to the external resistor for the ADIP amplifier control |
| 3 | WPP-LPF | | Connect terminal to the external capacitor for the TPP/WPP low-pass filter |
| 4 | VREF | О | Reference voltage output terminal |
| 5 | С | I | Signal (C) input from the optical pickup detector |
| 6 | D | I | Signal (D) input from the optical pickup detector |
| 7 | D-C | I | Signal (D) input from the optical pickup detector (AC input) |
| 8 | IY | I | I-V converted RF signal (IY) input from the optical pickup detector |
| 9 | IX | I | I-V converted RF signal (IX) input from the optical pickup detector |
| 10 | JX | I | I-V converted RF signal (JX) input from the optical pickup detector |
| 11 | JY | I | I-V converted RF signal (JY) input from the optical pickup detector |
| 12 | A | I | Signal (A) input from the optical pickup detector |
| 13 | A-C | I | Signal (A) input from the optical pickup detector (AC input) |
| 14 | В | I | Signal (B) input from the optical pickup detector |
| 15 | TON-C | _ | Connect terminal to the external capacitor for TON hold |
| 16 | CIG | _ | Connect terminal to the external capacitor for the low-pass filter of NPP divider denominator |
| 17 | CDN | _ | Connect terminal to the external capacitor for the low-pass filter of CSL divider denominator |
| 18 | PD-NI | I | Light amount monitor input terminal (non-invert input) |
| 19 | PD-I | I | Reference PWM signal input for the laser automatic power control from the system controller |
| 20 | PD-O | О | Light amount monitor output terminal |
| 21 | ADFG | О | ADIP duplex FM signal (22.05kHz ± 1kHz) output to the system controller |
| 22 | DVDD | | Power supply terminal (+2.3V) (digital system) |
| 23 | SBUS | I/O | SSB serial data input/output with the system controller |
| 24 | SCK | I | SSB serial clock signal input from the system controller |
| 25 | XRST | I | Reset signal input from the system controller "L": reset |
| 26 | OFTRK | I | Off track signal input terminal Not used |
| 27 | DGND | _ | Ground terminal (digital system) |
| 28 | BOTM | О | Bottom hold signal output of the light amount signal (RF/ABCD) to the system controller |
| 29 | PEAK | О | Peak hold signal output of the light amount signal (RF/ABCD) to the system controller |
| 30 | VREF075 | | Connect terminal to the external capacitor for the internal reference voltage |
| 31 | VC | О | Middle point voltage (+1.2V) generation output terminal |
| 32 | CCSL2 | _ | Connect terminal to the external capacitor for the TPP/WPP low-pass filter |
| 33 | RF OUT | О | Playback EFM RF signal output to the system controller |
| 34 | AGND | | Ground terminal (analog system) |
| 35 to 37 | EQ, LP, PS | | Connect terminal to the external capacitor for the RF equalizer |
| 38 | AVCC | | Power supply terminal (+2.3V) (analog system) |
| 39, 40 | OFC-2, OFC-1 | | Connect terminal to the external capacitor for the RF AC coupling |
| 41 | ABCD | О | Light amount signal (ABCD) output to the system controller |
| 42 | FE | О | Focus error signal output to the system controller |
| 43 | S-MON | О | Servo signal monitor output to the system controller |
| 44 | ADIP-IN | I | ADIP duplex FM signal (22.05kHz ± 1kHz) input terminal Not used |

• IC801 CXD2677-202GA (SYSTEM CONTROLLER, DIGITAL SIGNAL PROCESSOR, 16M BIT D-RAM)

| Pin No. | Din Noma | I/O | Description |
|----------|------------------|-----|---------------------------------------------------------------------------------------------------------------|
| | Pin Name | | Description Description |
| 1 | NC | 0 | Load address strobe signal output terminal for D-RAM Not used |
| 2 | NC | I | Test input terminal for D-RAM Not used |
| 3 to 7 | NC | 0 | Address signal output terminal for D-RAM Not used |
| 8, 9 | NC | I/O | Two-way data bus terminal for D-RAM Not used |
| 10, 11 | DRAMVDD0, 1 | _ | Power supply terminal (for D-RAM) (+2.4V) |
| 12, 13 | DRAMVSS0, 1 | | Ground terminal (for D-RAM) |
| 14, 15 | NC | I/O | Two-way data bus terminal for D-RAM Not used |
| 16 to 19 | NC | 0 | Address signal output terminal for D-RAM Not used |
| 20 | NC | 0 | Column address strobe signal output terminal for D-RAM Not used |
| 21 | NC | I | Test input terminal for D-RAM Not used |
| 22, 23 | NC | 0 | Address signal output terminal for D-RAM Not used |
| 24 | DVSS0 | _ | Ground terminal (for the DSP block) |
| 25 | DVDD0 | _ | Power supply terminal (for the DSP block) (+1.5V) |
| 26 | OFTRK | I | Off track signal input from the DSP monitor (3) |
| 27 | SSB DATA | I/O | SSB data input/output with the RF amplifier and the remote commander attached headphone |
| 28 | SSB CLK | 0 | SSB clock output to the RF amplifier |
| | SSD CLK | | PWM signal output for the Over write head drive power supply voltage control to the power |
| 29 | VREC PWM | 0 | control |
| 30 | VL PWM | 0 | PWM signal output for the laser power supply voltage control to the power control |
| 31 | VC PWM | 0 | PWM signal output for the system power supply voltage control to the power control |
| 32 | NC (VD PWM) | 0 | VD power supply voltage control signal output terminal Not used |
| 33 | NC | _ | Not used |
| 34 | IFVDD0 | _ | Power supply terminal (for the microcomputer I/F block) (+2.3V) |
| 35 | IFVSS0 | _ | Ground terminal (for the microcomputer I/F block) |
| 36 | OPT DET | I | DIN plug detection signal input terminal "H": DIN plug |
| 37 | XJACK DET | I | LINE IN plug detection signal input terminal "L": LINE or OPT plug |
| 38 | XMIC DET | I | Microphone plug detection signal input terminal "L": microphone plug |
| 39 | OPEN CLOSE SW | I | Open/close detection switch of the upper panel input terminal "L": when upper panel close |
| 40, 41 | SET CODE0, 1 | I | Input terminal for the set (open in this set) |
| 42, 43 | SET CODE2, 3 | I | Input terminal for the set (fixed at "L" in this set) |
| 44 | XPATCH | I | Patch function detection signal input terminal "L": patch function Not used |
| 45 | SI0 | I | Serial data input from the nonvolatile memory |
| 46 | SO0 | 0 | Serial data input from the nonvolatile memory, A/D converter and liquid crystal display element module |
| 47 | SCK0 | 0 | Serial clock signal output to the nonvolatile memory, A/D converter and liquid crystal display element module |
| 48 | XGUM ON | I | Rechargeable battery detection switch input terminal "L": there is rechargeable battery Not used |
| 49 | BEEP | 0 | Beep sound control signal output to the headphone amplifier |
| 50, 51 | TEST1, TEST0 | I | Input terminal for the main test (normally fixed at "L") |
| 52 | KDO | 0 | Data output terminal Not used |
| 53 | KRB | I | Ready/busy signal input terminal Not used |
| 54 | KCLK | 0 | Clock signal output terminal Not used |
| 55 | KCS | 0 | Chip select signal output terminal Not used |
| 56 | KDI | I | Data input terminal Not used |
| | KDI | 1 | Data input terminai 140t useu |

| S7 | |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------|
| S8 | |
| 59 VG CTL O VG power supply voltage control signal output terminal Not used 60 AOUT SEL O HP/LINE changeover signal output terminal Not used 61 REC OPR LED O LED ON/OFF control signal output terminal Not used 62 TSB SSB CTL O SB/SSB changeover control signal output Not used 63 GND SW2 O Battery for Cradle ON/OFF switch control signal output terminal Not used 64 CLK SEL O System clock select signal output to the power control 65 MIC SENSE O "L": Low sensitivity control signal output to the mic amplifier "L": Low sensitivity control signal output to the mic amplifier "L": Low sensitivity control signal output to the mic amplifier "L": Low sensitivity control signal output terminal 66 GND SW O GND changeover control signal output terminal 67 XCS LCD O Chip select signal output to the liquid crystal display element module 68 LCD STB O Strobe signal output to the liquid crystal display element module 69 MUTE O Analog muting control signal output for the headphone amplifier "H": muting O 70 CS RTC O Chip select signal (1) output to the nonvolatile memory 71 XCS NV1 O Chip select signal (1) output to the nonvolatile memory 72 IFVDD1 — Power supply terminal (for the microcomputer I/F block) (+2.3V) 73 IFVSS1 — Ground terminal (for the microcomputer I/F block) 74 XRST MTR 75 XRF RST O Reset control signal output to the RF amplifier "L": reset 76 SPDL MON 1 Spindle servo monitor signal input terminal 77 XHOLD SW I HOLD switch input terminal Not used 80,81 PD SO, PD S1 O PD IC mode changeover signal output to the optical pick up 82 PAUSE KEY I Pause key input terminal from the switch & liquid crystal display element module 83 PROTECT I Detection input terminal from the switch & liquid crystal display element module 84 SLD MON 1 Sled servo monitor signal output to the later diode drive to the power control 86 DVSS1 — Ground terminal (for the DSP block) 87 PAUSE KEY I Pause key input terminal from the switch & liquid crystal display element module 88 SLEEP O System sleep control signal output to the power control "H": sleep | |
| 60 AOUT SEL O HP/LINE changeover signal output terminal Not used 61 REC OPR LED O LED ON/OFF control signal output terminal Not used 62 TSB SSB CTL O TSB/SSB changeover control signal output Not used 63 GND SW2 O Battery for Cradle ON/OFF switch control signal output terminal Not used 64 CLK SEL O System clock select signal output to the power control 65 MIC SENSE O Mic sensitivity control signal output to the mic amplifier 66 GND SW O GND changeover control signal output to the mic amplifier 67 XCS LCD O Chip select signal output to the liquid crystal display element module 68 LCD STB O Strobe signal output to the liquid crystal display element module 69 MUTE O Analog muting control signal output for the headphone amplifier "H": muting O 70 CS RTC O Chip select signal (1) output to the nonvolatile memory 71 XCS NV1 O Chip select signal (1) output to the nonvolatile memory 72 IFVDD1 — Power supply terminal (for the microcomputer L/F block) 73 IFVSS1 — Ground terminal (for the microcomputer L/F block) 74 XRST MTR O Reset control signal output to the motor driver "L": reset 75 XRF RST O Reset control signal output to the RF amplifier "L": reset 76 SPDL MON I Spindle servo monitor signal input terminal 77 XHOLD SW I HOLD Switch input terminal "L": hold ON 78, 79 JOG A, B I Jog dial pulse input terminal "L": hold ON 82 PAUSE KEY I Pause key input terminal from the switch & liquid crystal display element module 83 PROTECT I Detection input terminal from the switch & liquid crystal display element module 84 SLD MON I Sled servo monitor signal input terminal 85 VLON O Power supply control signal output to the optical pick up 86 DVSSI — Ground terminal (for the DSP block) (+1.5V) 87 SRS STEP O System sleep control signal output to the power control "H": sleep ON 88 FFCLR O Input latch output for the bast whiching to the power control 90 CHG GAIN O Charge gain control signal output to the power control 91 CHG CTL O Charge ON/OFF control signal output to the power control 92 CHGI CTL O Charge ON/OFF control signal output | |
| 61 REC OPR LED O LED ON/OFF control signal output terminal Not used 62 TSB SSB CTL O TSB/SSB changeover control signal output Not used 63 GND SW2 O Battery for Cradle ON/OFF switch control signal output terminal Not used 64 CLK SEL O System clock select signal output to the power control 65 MIC SENSE O "L": Low sensitivity control signal output to the mic amplifier "L": Low sensitivity control signal output to the mic amplifier "L": Low sensitivity (normally: "H") Not used 66 GND SW O GND changeover control signal output terminal 67 XCS LCD O Chip select signal output to the liquid crystal display element module 68 LCD STB O Strobe signal output to the liquid crystal display element module 69 MUTE O Analog muting control signal output for the headphone amplifier "H": muting O 70 CS RTC O Chip select signal output terminal Not used 71 XCS NVI O Chip select signal (1) output to the nonvolatile memory 72 IFVDD1 — Power supply terminal (for the microcomputer L/F block) (+2.3V) 73 IFVSS1 — Ground terminal (for the microcomputer L/F block) 74 XRST MTR DRV O Reset control signal output to the RF amplifier "L": reset 75 XRF RST O Reset control signal output to the RF amplifier "L": reset 76 SPDL MON I Spindle servo monitor signal input terminal 77 XHOLD SW I HOLD switch input terminal Not used 80,81 PD S0, PD S1 O PD IC mode changeover signal output to the optical pick up 82 PAUSE KEY I Pause key input terminal from the switch & liquid crystal display element module 83 PROTECT I Detection input terminal from the switch & liquid crystal display element module 84 SLD MON I Sled servo monitor signal input terminal 85 VLON O Power supply control signal output to the power control "H": sleep ON 86 DVSS1 — Ground terminal (for the DSP block) 87 DVDD1 — Power supply terminal (for the DSP block) 88 SLEEP O System sleep control signal output to the power control "H": sleep ON 90 CHG GAIN O Charge gain control signal output to the power control "H": charge ON 91 CHG CTL O Charge ON/OFF control signal output to the power control | |
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| 91 CHG CTL O Charge ON/OFF control signal output to the power control "H": charge ON 92 CHGI CTL O Charge current control signal output terminal Not used 93 XHP STBY O Power supply control signal output for the head phone | |
| 92 CHGI CTL O Charge current control signal output terminal Not used 93 XHP STBY O Power supply control signal output for the head phone | |
| 93 XHP STBY O Power supply control signal output for the head phone | |
| | |
| 94 XCS NV2 O Chip select signal (2) output terminal Not used | |
| | |
| 95 IFVSS2 — Ground terminal (for the microcomputer I/F block) | |
| 96 IFVDD2 — Power supply terminal (for the microcomputer I/F block) (+2.3V) | |
| 97 T MARK SW I T MARK (track mark) switch input terminal "L": track mark detection Not use | ed |
| 98 NC O LED ON/OFF control signal output terminal for CHG (charge display) Not used | d |
| 99 NC I Initial switch detection input terminal Not used | |
| 100 NC — Not used | |
| 101 XUDP UP ON O Pullup resistor changeover control signal output terminal Not used | |
| 102 LCD RST O Reset control signal output to the liquid crystal display element module "L": rese | et |
| 103 XMUTE O Analog muting control signal output terminal "L": muting ON Not used | |

| Pin No. | Pin Name | I/O | Description |
|---------------|-----------------|-----|------------------------------------------------------------------------------------------------------------------------------|
| 104 | XRST | I | System reset signal input from the power control "L": reset |
| 105 | STAND DET | I | Charging stand detection signal input terminal |
| 106 | VB MON | I | Voltage monitor input terminal (A/D input) of the UNREG power supply |
| 107 | | I | Charge voltage monitor input (A/D input) from the power control |
| - | CHG MON | | |
| 108 | VREF MON | I | Clear reference voltage monitor input terminal (A/D input) from the RF amplifier |
| 109, 110 | SET KEY 1, 2 | I | Key input terminal (A/D input) from the switch & liquid crystal display element module |
| 111 | VBUS DET | I | USB power supply voltage detection signal input terminal |
| 112 | HIDC MON | I | HIGH DC voltage monitor input terminal (A/D input) |
| 113 | WK DET | I | Set key and USB start switching detection signal input terminal (A/D input) |
| 114 | REC KEY | I | REC key input terminal (A/D input) |
| 115 | HALF LOCK SW | I | Open button detection switch input terminal (A/D input) Input "L" when the open button is pressed. Input "H" in other cases. |
| 116 | | I | Key input terminal (A/D input) from the remote commander attached headphone |
| | RMC KEY | | |
| 117 | AVDD | _ | Power supply terminal (for the microcomputer analog) (+2.8V) |
| 118 | AVSS | | Ground terminal (for the microcomputer analog) Reverse symply terminal (for the TSR master communication) (12.9V) |
| 119 | TSMVDD | | Power supply terminal (for the TSB master communication) (+2.8V) |
| 120 | RMC DTCK | I/O | Serial data input/output with the remote commander attached headphone |
| 121 | TSLVDD | _ | Power supply terminal (for the I/F to TSB slave) (+2.3V) |
| 122, 123 | NC | | Not used |
| 124 | TAT | | Not used |
| 125 | TAN | | Not used |
| 126 | NAR | | Not used |
| 127 | ID0 | | Not used |
| 128 | SAK | | Not used |
| 129 | IT0 | | Not used |
| 130 | MITY | | Ground terminal Not used |
| 131 | SUSPEND | О | USB suspend signal output terminal |
| 132 | USBIFVDD | | Power supply terminal (for USB I/F) (+3.3V) |
| 133 | UDM | I | USB data (-) input terminal |
| 134 | UDP | I | USB data (+) input terminal |
| 135 | UPUEN | O | USB pullup resistor connection control output terminal |
| 136 | USBOSCVDD | | Power supply terminal (for the USB oscillation circuit) (+2.4V) |
| 137 | UOSCI | I | Clock (48MHz) input terminal for the USB |
| 138 | UOSCO | О | Clock (48MHz) output terminal for the USB |
| 139 | USBOSCVSS | | Ground terminal (for the USB oscillation circuit) |
| 140 to 142 | MODE1 to 3 | О | Power supply control signal output for the over write head to the over write head drive |
| 143 | HD CON 1 | О | Over write head control signal output to the over write head drive |
| 144 | PBVDD | | Power supply terminal (+2.3V) |
| 145 | HD CON 2 | О | Over write head control signal output to the over write head drive |
| 146 | XTEST | I | Input terminal for test mode set (normally: open) "L": test mode |
| 147 | XCS ADA | 0 | Chip select signal output to the A/D converter |
| 148 | XPD ADA | 0 | Power supply control signal output for the drive to the A/D converter |
| 149 | VDIOSC | | Power supply terminal (for the OSC cell) (+2.4V) |
| 150 | OSCI | I | System clock (45.1584MHz) input terminal |
| 151 | OSCO | 0 | System clock (45.1584MHz) output terminal |
| 152 | VSIOSC | | Ground terminal (for the OSC cell) |
| 152 | 101000 | | Ground terminal (rot the COC con) |

| Pin No. | Pin Name | I/O | Description |
|---------|----------|-----|-------------------------------------------------------------------------------------------------------------------------|
| 153 | DAVDD | _ | Power supply terminal (for the built-in D/A converter) (+2.4V) |
| 154 | VREFL | I | Reference voltage input terminal (for the built-in D/A converter L-CH) |
| 155 | AOUTL | О | Built-in D/A converter (L-CH) output terminal |
| 156 | AOUTR | О | Built-in D/A converter (R-CH) output terminal |
| 157 | VREFR | I | Reference voltage input terminal (for the built-in D/A converter R-CH) |
| 158 | DAVSS | _ | Ground terminal (for the built-in D/A converter) |
| 159 | ASYO | О | Playback EFM duplex signal output terminal |
| 160 | ASYI | I | Playback EFM comparison slice level input terminal |
| 161 | AVD1 | | Power supply terminal (for the DSP asymmetry system analog) (+2.4V) |
| 162 | BIAS | I | Bias current input terminal for the playback EFM comparison |
| 163 | RFI | I | Playback EFM the RF signal input from the RF amplifier |
| 164 | AVS1 | | Ground terminal (for the DSP asymmetry system analog) |
| 165 | PCO | 0 | Phase comparison output terminal for the playback EFM system master PLL |
| 166 | FILI | I | Filter input terminal for the playback EFM system master PLL |
| 167 | FILO | 0 | Filter output terminal for the playback EFM system master PLL |
| 168 | CLTV | | Internal VCO control voltage input terminal for the playback EFM system master PLL |
| 169 | PEAK | I | Peak hold signal input of the light amount signal (RF/ABCD) from the RF amplifier |
| | BOTM | I | |
| 170 | | | Bottom hold signal input of the light amount signal (RF/ABCD) from the RF amplifier |
| 171 | ABCD | I | Light amount signal (ABCD) input from the RF amplifier |
| 172 | FE | I | Focus error signal input from the Focus error amplifier |
| 173 | AUX1 | I | Support signal (I3 signal/temperature signal) input terminal (A/D input) |
| 174 | VC | I | Middle point voltage (+1.1V) input terminal |
| 175 | ADIO | O | Monitor output terminal of A/D converter input signal Not used |
| 176 | ADRT | I | A/D converter the upper limit voltage input terminal (fixed at "H" in this set) |
| 177 | AVD2 | | Power supply terminal (for the DSP servo analog system) (+2.4V) |
| 178 | AVS2 | | Ground terminal (for the DSP servo analog system) |
| 179 | ADRB | I | A/D converter the lower limit voltage input terminal (fixed at "L" in this set) |
| 180 | SE | I | Servo signal monitor input terminal (A/D input) from the RF amplifier |
| 181 | TE | I | Tracking error signal input from the Tracking error amplifier |
| 182 | DCHG | | Connecting terminal with the analog power supply of the low impedance (fixed at "H" in this set) |
| 183 | APC | I | Error signal input for the laser automatic power control (fixed at "H" in this set) |
| 184 | CKRF | О | Clock output terminal for the RF amplifier control Not used |
| 185 | DTRF | О | Data output terminal for the RF amplifier control Not used |
| 186 | XLRF | О | Latch signal output terminal for the RF amplifier control Not used |
| 187 | DVSS2 | | Ground terminal (for the DSP block) |
| 188 | DVDD2 | | Power supply terminal (for the DSP block) (+1.5V) |
| 189 | XTSL | I | Input terminal for the frequency set up of the system clock "L": 45.1584MHz, "H": 22.5792MHz (fixed at "L" in this set) |
| 190 | DIN1 | I | Input terminal of the record system digital audio signal |
| 191 to | NC | О | D/A converter PWM signal output terminal Not used |
| 193 | | | |
| 194 | DADT | 0 | Audio data output terminal Not used |
| 195 | ADDT | I | Data input from the external A/D converter |
| 196 | LRCK | 0 | L/R sampling clock signal (44.1KHz) output to the external A/D converter |
| 197 | XBCK | 0 | Bit clock signal (2.8224MHz) output to the external A/D converter |
| 198 | FS256 | 0 | 11.2896MHz clock signal output to the external A/D converter |
| 199 | NC | I | Ground terminal Clock signal input from the external VCO Not used |

| Pin No. | Pin Name | I/O | Description |
|------------|------------|-----|--------------------------------------------------------------------------------------------------------|
| 200 | DVSS3 | | Ground terminal (for the DSP block) |
| 201 | DVDD3 | | Power supply terminal (for the DSP block) (+1.5V) |
| 202 | ADFG | I | ADIP duplex FM signal (20.05±1kHz) input from the RF amplifier |
| 203 | NC | 0 | Filter cut off control signal output terminal Not used |
| 204 | IFVDD3 | | Power supply terminal (for the microcomputer I/F block) (+2.3V) |
| 205 | IFVSS3 | | Ground terminal (for the microcomputer I/F block) |
| 206 | APCREF | 0 | Reference PWM signal output for the laser automatic power control to the RF amplifier |
| 207 | TRDR | 0 | Tracking servo drive PWM signal output (–) to the coil driver |
| 208 | TFDR | 0 | Tracking servo drive PWM signal output (+) to the coil driver |
| 209 | FFDR | 0 | Focus servo drive PWM signal output (+) to the coil driver |
| 210 | FRDR | 0 | Focus servo drive PWM signal output (-) to the coil driver |
| 211 | FS4 | 0 | 176.4kHz clock signal output to the power control |
| 212 | SPRD | 0 | Spindle motor drive control signal output (U) to the motor driver |
| 213 | SPFD | 0 | Spindle servo drive PWM signal output to the motor driver |
| 214 | SPDV | 0 | Spindle motor drive control signal output (V) to the motor driver |
| 215 | SPDW | 0 | Spindle motor drive control signal output (W) to the motor driver |
| 216 | SPCU | I | Spindle motor drive comparison signal input (U) from the motor driver |
| 217 | SPCV | I | Spindle motor drive comparison signal input (V) from the motor driver |
| 218 | SPCW | I | Spindle motor drive comparison signal input (W) from the motor driver |
| 219 | SRDR | 0 | Sled motor drive control signal output (U) to the motor driver |
| 220 | SFDR | О | Sled servo drive PWM signal output to the motor driver |
| 221 | SLDV | О | Sled motor drive control signal output (V) to the motor driver |
| 222 | SLDW | О | Sled motor drive control signal output (W) to the motor driver |
| 223 | DVSS4 | _ | Ground terminal (for the DSP block) |
| 224 | DVDD4 | _ | Power supply terminal (for the DSP block) (+1.5V) |
| 225 | SLCU | I | Sled motor drive comparison signal input (U) from the motor driver |
| 226 | SLCV | I | Sled motor drive comparison signal input (V) from the motor driver |
| 227 | SLCW | I | Sled motor drive comparison signal input (W) from the motor driver |
| 228 | IFVDD4 | _ | Power supply terminal (for the microcomputer I/F block) (+2.3V) |
| 229 | IFVSS4 | | Ground terminal (for the microcomputer I/F block) |
| 230 | EFMO | О | EFM encode data output for the record to the over write head drive |
| 231 to 233 | MNT0 to 2 | О | DSP monitor (0) to (2) output terminal Not used |
| 234 | MNT3 | О | Off track signal output from the DSP monitor (3) |
| 235 | SENSE | О | DSP internal status (DSP SENS monitor) signal output terminal Not used |
| 236 | TX | 0 | Record data output enable signal output monitor terminal of the DSP Not used |
| 237 | RECP | О | Laser power changeover signal output terminal Not used |
| 238 | LRCKI/XELT | I | Input terminal for the PCM data I/F/ ATRAC data I/F Not used |
| 239 | XBCKI/ECK | I | Input terminal for the PCM data I/F/ ATRAC data I/F Not used |
| 240 | DATAI/EDT | I | Input terminal for the PCM data I/F/ ATRAC data I/F Not used |
| 241 | XERQ | I | Input terminal for the ATRAC data I/F Not used |
| 242 | A11 | О | Address signal output terminal for D-RAM Not used |
| 243 | XOE | 0 | Output enable signal output terminal for D-RAM Not used |
| 244 | XWE | О | Data write enable signal output terminal for D-RAM Not used |
| 245 | TSTDR3 | I | Test input terminal for D-RAM Not used |
| 246 | EVA | I | EVA/FLASH chip discrimination input terminal "L": FLASH chip, "H": EVA chip (fixed at "L" in this set) |

MZ-N707

| Pin No. | Pin Name | I/O | Description |
|---------------|----------|-----|---------------------------------------------------------------|
| 247 | FLASHVDD | _ | Power supply terminal (for the built-in flash memory) (+2.4V) |
| 248 | FLASHVSS | _ | Ground terminal (for the built-in flash memory) |
| 249 to 256 | NC | _ | Not used |

SECTION 7 EXPLODED VIEWS

NOTE:

- -XX and -X mean standardized parts, so they may have some difference from the original one.
- Color Indication of Appearance Parts Example:

KNOB, BALANCE (WHITE) . . . (RED) \uparrow

Parts Color Cabinet's Color

• Abbreviation CND : Canadian model FR : French model

- Items marked "*" are not stocked since they are seldom required for routine service. Some delay should be anticipated when ordering these items.
- The mechanical parts with no reference number in the exploded views are not supplied.
- Accessories are given in the last of the electrical parts list.

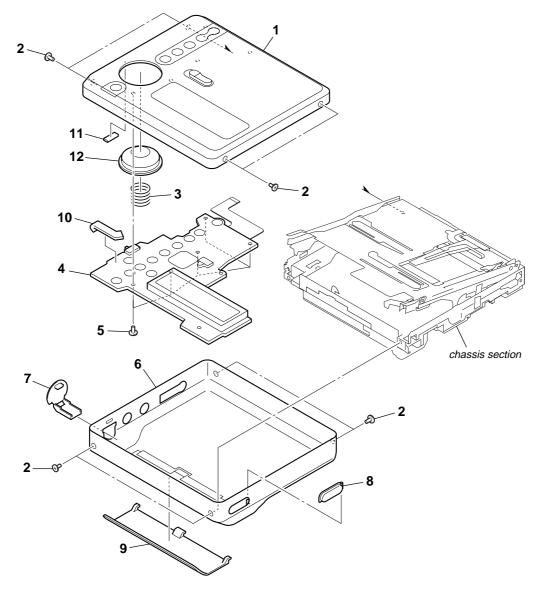
The components identified by mark \triangle or dotted line with mark \triangle are critical for safety.

critical for safety.
Replace only with part number specified.

Les composants identifiés par une marque \triangle sont critiquens pour la sécurité

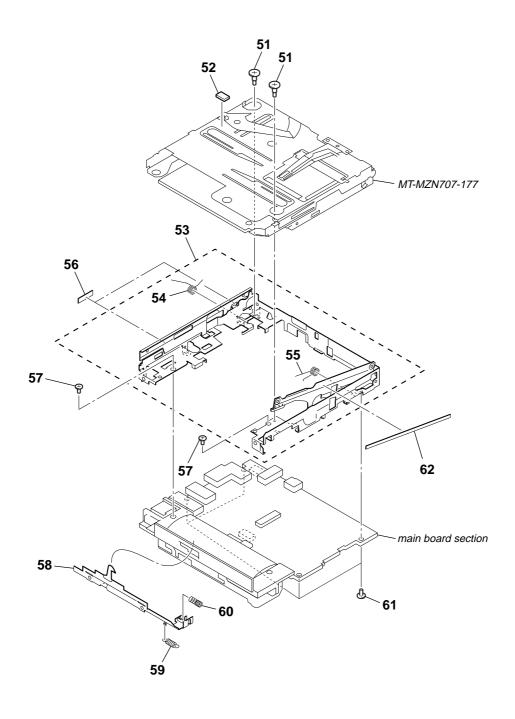
sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

7-1. UPPER PANEL, CASE (LOWER) SECTION



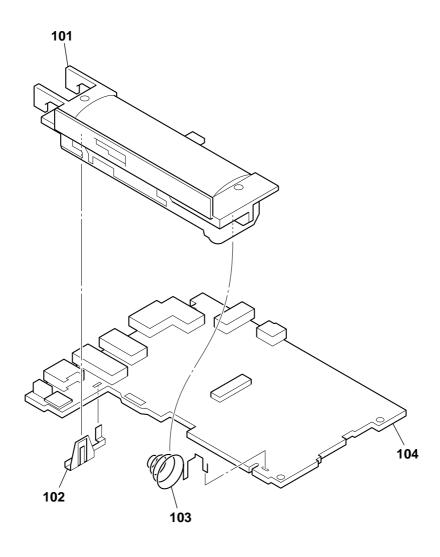
| Ref. No. | Part No. | Description | <u>Remark</u> | Ref. No. | Part No. | Description | <u>Remark</u> |
|----------|--------------|-----------------------------------|---------------|----------|--------------|-----------------------------------|---------------|
| 1 | X-3381-803-1 | PANEL (S) SUB ASSY, UPPER (SILVE | R) | 6 | 3-237-098-41 | CASE (LOWER) (BLACK) (AEP, UK, FI | R) |
| 1 | X-3381-804-1 | PANEL (L) SUB ASSY, UPPER (BLUE) |) | 6 | 3-237-098-51 | CASE (LOWER) (BLACK) (US, CND) | |
| 1 | X-3381-805-1 | PANEL (B) SUB ASSY, UPPER (BLAC | K) | 7 | 3-237-100-01 | CAP (P) (for BLUE, SILVER) | |
| 2 | 3-234-449-17 | SCREW (M1.4) (for BLACK) | | 7 | 3-237-100-11 | CAP (P) (for BLACK) | |
| 2 | 3-234-449-19 | SCREW (M1.4) (for BLUE, SILVER) | | 8 | 3-237-099-01 | BUTTON (OPEN) | |
| | | | | | | | |
| 3 | 3-237-094-01 | SPRING (BUTTON), COMPRESSION | | 9 | | LID, BATTERY CASE (SILVER) | |
| 4 | 1-804-543-11 | LCD MODULE | | 9 | 3-225-636-31 | LID, BATTERY CASE (BLUE) | |
| 5 | 3-318-382-91 | SCREW (1.7X2.5), TAPPING | | 9 | 3-225-636-41 | LID, BATTERY CASE (BLACK) | |
| 6 | 3-237-098-01 | CASE (LOWER) (SILVER) (AEP, UK, F | R) | 10 | 3-238-582-01 | SHEET (GROUND) | |
| 6 | 3-237-098-11 | CASE (LOWER) (SILVER) (US, CND) | , | 11 | 3-239-790-01 | SPACER (UP) | |
| | | | | | | | |
| 6 | | CASE (LOWER) (BLUE) (AEP, UK, FR) |) | 12 | 3-237-093-01 | BUTTON (CONTROL B) | |
| 6 | 3-237-098-31 | CASE (LOWER) (BLUE) (US, CND) | | | | | |

7-2. CHASSIS SECTION



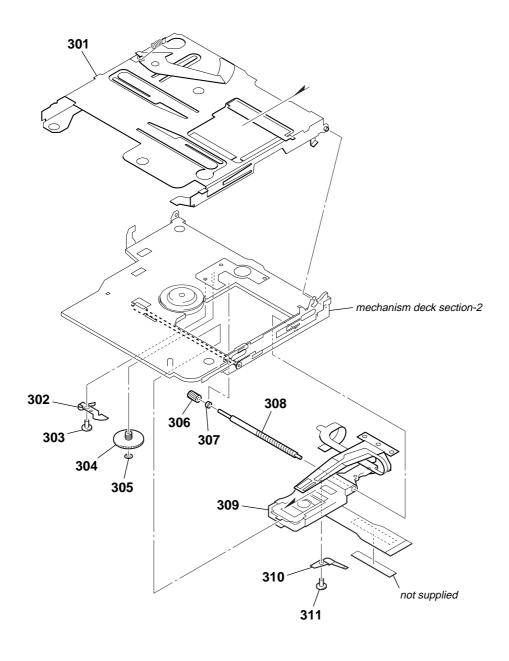
| Ref. No. | Part No. | Description | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> |
|----------|--------------|----------------------------|---------------|----------|--------------|--------------------------------|---------------|
| 51 | 3-237-072-01 | SCREW (MD), STEP | | 57 | 3-318-382-91 | SCREW (1.7X2.5), TAPPING | |
| 52 | 3-238-127-01 | SPACER (HOLDER) | | 58 | 3-237-080-01 | SLIDER, OPEN | |
| 53 | X-3381-652-1 | CHASSIS (5192) ASSY, SET | | 59 | 3-237-082-01 | SPRING (LOCK), TENSION | |
| 54 | 3-237-075-01 | SPRING (POP UP-L), TORSION | | 60 | 3-237-081-01 | SPRING (LIMITTER), COMPRESSION | |
| 55 | 3-237-083-01 | SPRING (POP UP-R), TORSION | | 61 | 3-335-797-01 | SCREW (M1.4X2), TOOTHED LOCK | |
| | | | | | | | |
| 56 | 3-239-791-01 | SHEET (FULCRUM PLATE LA) | | 62 | 3-239-800-01 | SHEET (FULCRUM PLATE RA) | |

7-3. MAIN BOARD SECTION



| Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> |
|----------|--------------|-----------------------------|---------------|----------|--------------|------------------------------|---------------|
| 101 | 3-237-079-01 | CASE, BATTERY | | * 104 | X-3381-933-1 | MAIN BOARD, COMPLETE (EXCEPT | US, FR) |
| 102 | 3-237-073-01 | TERMINAL BOARD (+), BATTERY | | * 104 | X-3381-967-1 | MAIN BOARD, COMPLETE (US) | |
| 103 | 3-237-074-01 | TERMINAL (-). BATTERY | | * 104 | X-3382-202-1 | MAIN BOARD, COMPLETE (FR) | |

7-4. MECHANISM DECK SECTION-1 (MT-MZN707-177)

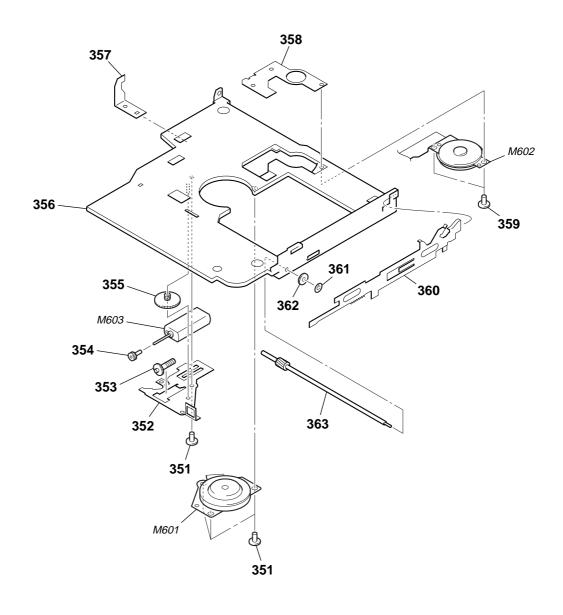


The components identified by mark \triangle or dotted line with mark \triangle are critical for safety. Replace only with part number specified.

Les composants identifiés par une marque ⚠ sont critiques pour la sécurité.
Ne les remplacer que par une pièce portant le numéro spécifié.

| Ref. No. | Part No. | Description | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> |
|----------|--------------|----------------------------------|---------------|-----------|--------------|----------------------------------|---------------|
| 301 | X-3381-219-1 | HOLDER ASSY | | 307 | 3-043-237-02 | BEARING (N) | |
| 302 | 3-224-779-02 | SPRING, THRUST DETENT | | 308 | 4-222-203-02 | SCREW, LÈAD | |
| 303 | 3-225-996-01 | SCREW (M1.4) (EG), PRECISION PAN | J | 1 309 1 € | X-3381-589-1 | SERVICE ASSY, OP (LCX-5R) | |
| 304 | 4-222-216-01 | GEAR (SA) | | 310 | 3-049-336-03 | SPRING (S), RACK | |
| 305 | 3-338-645-31 | WASHER (0.8-2.5) | | 311 | 3-225-996-06 | SCREW (M1.4) (EG), PRECISION PAR | V |
| | | | | | | | |
| 306 | 4-222-208-01 | GEAR (SB) | | | | | |

7-5. MECHANISM DECK SECTION-2 (MT-MZN707-177)



| Ref. No. | Part No. | Description | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> |
|----------|--------------|----------------------------------|---------------|----------|--------------|------------------------------|---------------|
| 351 | 3-225-278-11 | SCREW, TAPPING | | 360 | 3-235-839-01 | LEVER (RACK) | |
| 352 | 3-235-838-01 | COVER, MOTOR | | 361 | 3-338-645-31 | WASHER (0.8-2.5) | |
| 353 | 3-235-836-01 | GEAR (HB) | | 362 | 4-222-222-01 | GEAR (RACK) | |
| 354 | 3-222-544-01 | GEAR (HA) | | 363 | A-3174-089-A | SHAFT BLOCK ASSY, SUB | |
| 355 | 3-235-835-01 | GEAR (HC) | | M601 | 8-835-744-01 | MOTOR, DC SSM18B (SPINDLE) | |
| | | | | | | (WITH T | URN TABLE) |
| 356 | 3-235-834-01 | CHASSIS | | | | | |
| 357 | 3-235-830-01 | PLATE, RATCHET | | M602 | 1-763-727-11 | MOTOR, DC (SLED) (WITH GEAR) | |
| 358 | X-3379-529-4 | BASE ASSY, MOTOR | | M603 | 1-763-400-21 | MOTOR, DC (OVER WRITE HEAD L | JP/DOWN) |
| 359 | 3-225-996-07 | SCREW (M1.4) (EG), PRECISION PAI | N | | | | |

MAIN

SECTION 8 ELECTRICAL PARTS LIST

NOTE:

- · Due to standardization, replacements in the parts list may be different from the parts specified in the diagrams or the components used on the set.
- -XX and -X mean standardized parts, so they may have some difference from the original one.
- RESISTORS

All resistors are in ohms. METAL: Metal-film resistor.

METAL OXIDE: Metal oxide-film resistor.

F: nonflammable

FR: French model

• Abbreviation CND: Canadian model · Items marked "*" are not stocked since they are seldom required for routine service. Some delay should be anticipated when ordering these items.

SEMICONDUCTORS

In each case, u: μ , for example:

 $\begin{array}{ll} uA. \ \ \, : \mu A. \ \ \, . \\ uPB. \ \ \, : \mu PB. \ \ \, . \\ uPD. \ \ \, : \mu PD. \ \ \, . \end{array}$ $uPA.\ .\ : \mu PA.\ .$ $uPC..: \mu PC..$

 CAPACITORS uF: μF

COILS uH: μH The components identified by mark \triangle or dotted line with mark \triangle are critical for safety. Replace only with part number specified.

Les composants identifiés par une marque \triangle sont critiquens pour la

sécurité. Ne les remplacer que par une pièce portant le numéro spécifié.

When indicating parts by reference number, please include the board.

| Ref. No. | Part No. | <u>Description</u> | | | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | | | <u>Remark</u> |
|--------------|--------------|--------------------|----------------------|-----------|---------------|--------------|--------------|--------------------|----------|-------|---------------|
| * | X-3381-933-1 | MAIN BOARD, CO | MPLETE (F | XCEPT I | IS FR) | | | | | | |
| * | X-3381-967-1 | MAIN BOARD, CO | MPLETE (L | .XOLI I (| 30, 111) | C310 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| * | | MAIN BOARD, CO | | | | C311 | | CERAMIC CHIP | 0.00 Tul | 10% | 10V |
| | X-0002-202-1 | ****** | , | 11) | | C312 | | CERAMIC CHIP | 2.2uF | 10% | 10V 10V |
| | | | | | | C314 | | TANTALUM CHIP | | 20% | 6.3V |
| | | < CAPACITOR > | | | | C315 | | CERAMIC CHIP | 1uF | 10% | 6.3V |
| | | < GAFAGITUR > | | | | 0313 | 1-120-007-11 | CENAIVIIC CHIP | TUF | 10 /0 | 0.37 |
| C101 | 1-107-823-11 | CERAMIC CHIP | 0.47uF | 10% | 16V | C316 | 1-135-201-11 | TANTALUM CHIP | 10uF | 20% | 4V |
| | | | | | (US) | C317 | 1-119-750-11 | TANTALUM CHIP | 22uF | 20% | 6.3V |
| C101 | 1-125-889-11 | CERAMIC CHIP | 2.2uF | 10% | 10V | C318 | 1-110-569-11 | TANTALUM CHIP | 47uF | 20% | 6.3V |
| | | | | (E) | (CEPT US) | C319 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C102 | 1-164-941-11 | CERAMIC CHIP | 0.0047uF | 10% | 16V | C320 | 1-135-151-21 | TANTALUM CHIP | 4.7uF | 20% | 4V |
| C103 | 1-164-939-11 | CERAMIC CHIP | 0.0022uF | 10% | 50V | | | | | | |
| C104 | 1-135-868-11 | TANTALUM CHIP | 220uF | 20% | 2.5V | C321 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| | | | | | | C322 | 1-110-569-11 | TANTALUM CHIP | 47uF | 20% | 6.3V |
| C105 | 1-115-467-11 | CERAMIC CHIP | 0.22uF | 10% | 10V | C323 | 1-135-259-11 | TANTALUM CHIP | 10uF | 20% | 6.3V |
| C106 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V | C324 | 1-135-259-11 | TANTALUM CHIP | 10uF | 20% | 6.3V |
| C110 | | CERAMIC CHIP | 1uF | 10% | 6.3V | C325 | | CERAMIC CHIP | 0.001uF | 10% | 50V |
| C111 | | CERAMIC CHIP | 100PF | 5% | 50V | | | | | | |
| C112 | | CERAMIC CHIP | 1uF | 10% | 6.3V | C326 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| 0 | 20 00 | 02 | | | 0.01 | C327 | 1-164-937-11 | | 0.001uF | 10% | 50V |
| C113 | 1-131-862-11 | TANTALUM CHIP | 4711F | 20% | 4V | C330 | 1-107-820-11 | | 0.1uF | 1070 | 16V |
| C116 | | CERAMIC CHIP | 0.001uF | 10% | 50V | C331 | 1-164-937-11 | | 0.001uF | 10% | 50V |
| C117 | | CERAMIC CHIP | 0.47uF | 10% | 10V | C332 | | TANTALUM CHIP | | 20% | 6.3V |
| C118 | | CERAMIC CHIP | 0.0022uF | | 50V | 0002 | 1 100 200 11 | TANTALOW OTH | Toul | 2070 | 0.0 V |
| C201 | | CERAMIC CHIP | 0.0022ui | 10% | 16V | C334 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| 0201 | 1-107-023-11 | CENAIVIIC CITIF | 0.47 ui | 10 /0 | (US) | C335 | 1-135-259-11 | | | 20% | 6.3V |
| | | | | | (03) | C336 | 1-164-933-11 | CERAMIC CHIP | 220PF | 10% | 50V |
| C201 | 1 105 000 11 | CERAMIC CHIP | 2.2uF | 10% | 10V | C340 | 1-125-777-11 | | 0.1uF | 10% | 10V |
| 0201 | 1-125-009-11 | CENAIVIIC CITIF | Z.Zui | | (CEPT US) | C501 | | CERAMIC CHIP | 100PF | 5% | 50V |
| C202 | 1 164 041 11 | CERAMIC CHIP | 0.0047uF | 10% | 16V | 0301 | 1-104-074-11 | GENAIVIIG GHIF | TOUFF | J /0 | 30 V |
| | | CERAMIC CHIP | 0.0047uF 0.0022uF | | 50V | CEOO | 1-107-819-11 | CERAMIC CHIP | 0.022uF | 10% | 16V |
| C203 C204 | | TANTALUM CHIP | 220uF | | 2.5V | C502 C503 | 1-164-939-11 | | | 10% | 50V |
| | | | | 20% | | | | | | | |
| C205 | 1-110-407-11 | CERAMIC CHIP | 0.22uF | 10% | 10V | C504 | 1-164-939-11 | CERAMIC CHIP | | 10% | 50V |
| 0000 | 1 104 007 11 | OEDAMIO OLUD | 0.004 | 100/ | F0\/ | C505 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C206 | | CERAMIC CHIP | 0.001uF | 10% | 50V | C506 | 1-107-819-11 | CERAMIC CHIP | 0.022uF | 10% | 16V |
| C210 | | CERAMIC CHIP | 1uF | 10% | 6.3V | 0507 | 4 407 040 44 | OED ANALO OLUB | 0.000 F | 400/ | 40)/ |
| C211 | | CERAMIC CHIP | 100PF | 5% | 50V | C507 | 1-107-819-11 | | 0.022uF | 10% | 16V |
| C212 | | CERAMIC CHIP | 1uF | 10% | 6.3V | C508 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C213 | 1-131-862-11 | TANTALUM CHIP | 4/ur | 20% | 4V | C509 | 1-164-939-11 | | 0.0022uF | 10% | 50V |
| 0010 | 4 404 007 44 | 0504440 01110 | 0.004 5 | 400/ | 50) <i>(</i> | C510 | 1-164-850-11 | | 10PF | 0.5PF | 50V |
| C216 | | CERAMIC CHIP | 0.001uF | 10% | 50V | C511 | 1-164-850-11 | CERAMIC CHIP | 10PF | 0.5PF | 50V |
| C217 | | CERAMIC CHIP | 0.47uF | 10% | 10V | | | | | | |
| C218 | | CERAMIC CHIP | 0.0022uF | | 50V | C512 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C301 | | TANTALUM CHIP | | 20% | 6.3V | C513 | | CERAMIC CHIP | 10PF | 0.5PF | 50V |
| C302 | 1-107-820-11 | CERAMIC CHIP | 0.1uF | | 16V | C514 | | CERAMIC CHIP | 0.022uF | 10% | 16V |
| | | | | | | C515 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C305 | 1-135-149-21 | TANTALUM CHIP | 2.2uF | 20% | 10V | C516 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C306 | 1-107-820-11 | CERAMIC CHIP | 0.1uF | | 16V | | | | | | |
| C307 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C518 | 1-127-760-11 | CERAMIC CHIP | 4.7uF | 10% | 6.3V |
| C308 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C519 | 1-164-941-11 | CERAMIC CHIP | 0.0047uF | 10% | 16V |
| C309 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V | C521 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| | | | | | | | | | | | |

MAIN

| Ref. No. | Part No. | <u>Description</u> | | | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | | | <u>Remark</u> |
|----------|--------------|--------------------|----------|------|---------------|----------|--------------|--------------------|---------------------|--------|---------------|
| C522 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C803 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C523 | | CERAMIC CHIP | 1uF | 10% | 6.3V | C804 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| 0020 | 1 120 007 11 | OLI WINIO OTTI | · ui | 1070 | 0.01 | C805 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V |
| C524 | 1-135-259-11 | TANTALUM CHIP | 10υΕ | 20% | 6.3V | C806 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V |
| C527 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V | C807 | | CERAMIC CHIP | 10PF | 0.5PF | 50V |
| C527 | 1-119-923-11 | TANTALUM CHIP | | 20% | 6.3V | 0007 | 1-104-050-11 | GENAIVIIG GHIF | TUFF | 0.3FF | 30 V |
| | 1-164-939-11 | CERAMIC CHIP | 0.0022uF | | 50V | C808 | 1 164 050 11 | CEDAMIC CHID | 10PF | 0.5PF | 50V |
| C530 | | | | 10% | | | | CERAMIC CHIP | | | |
| C531 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C809 | 1-137-739-11 | TANTALUM | 22uF | 20% | 6.3V |
| 0554 | 4 405 777 44 | 0554440 01115 | 0.4 5 | 100/ | 4017 | C810 | | CERAMIC CHIP | 1uF | 10% | 6.3V |
| C551 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C811 | | CERAMIC CHIP | 1uF | 10% | 6.3V |
| C557 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V | C812 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C558 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V | | | | | | |
| C559 | 1-119-923-11 | CERAMIC CHIP | 0.047uF | 10% | 10V | C813 | 1-125-891-11 | CERAMIC CHIP | 0.47uF | 10% | 10V |
| C561 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C814 | 1-164-935-11 | CERAMIC CHIP | 470PF | 10% | 50V |
| | | | | | | C815 | 1-110-569-11 | TANTALUM CHIP | 47uF | 20% | 6.3V |
| C562 | 1-107-819-11 | CERAMIC CHIP | 0.022uF | 10% | 16V | C816 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C563 | 1-107-819-11 | CERAMIC CHIP | 0.022uF | 10% | 16V | C817 | 1-109-982-11 | CERAMIC CHIP | 1uF | 10% | 10V |
| C564 | 1-107-819-11 | CERAMIC CHIP | 0.022uF | 10% | 16V | | | | | | |
| C565 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C818 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C566 | | CERAMIC CHIP | 0.01uF | 10% | 16V | C819 | 1-107-819-11 | CERAMIC CHIP | 0.022uF | 10% | 16V |
| 0000 | 1 101 010 11 | OZIWANIO OIIII | 0.0141 | 1070 | 101 | C820 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C567 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C821 | 1-137-740-11 | | 47uF | 20% | 6.3V |
| C601 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C822 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C602 | 1-104-943-11 | CERAMIC CHIP | 0.01uF | 10% | 10V 10V | 0022 | 1-125-111-11 | GENAIVIIG GHIF | U.Tur | 10 /0 | 100 |
| | | | | | | 0000 | 1 107 000 11 | CEDAMIC CUID | 0.1 | | 101 |
| C603 | 1-128-964-11 | TANTALUM CHIP | 100uF | 20% | 6.3V | C823 | | CERAMIC CHIP | 0.1uF | 4.00/ | 16V |
| C604 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C824 | | CERAMIC CHIP | 0.047uF | 10% | 10V |
| | | | | | | C825 | | CERAMIC CHIP | 100PF | 5% | 50V |
| C605 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V | C826 | | CERAMIC CHIP | 0.22uF | 10% | 10V |
| C606 | 1-164-874-11 | CERAMIC CHIP | 100PF | 5% | 50V | C828 | 1-125-837-11 | CERAMIC CHIP | 1uF | 10% | 6.3V |
| C607 | 1-164-874-11 | CERAMIC CHIP | 100PF | 5% | 50V | | | | | | |
| C608 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C829 | 1-125-837-11 | CERAMIC CHIP | 1uF | 10% | 6.3V |
| C609 | 1-131-862-11 | TANTALUM CHIP | 47uF | 20% | 4V | C830 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| | | | | | | C832 | 1-164-850-11 | CERAMIC CHIP | 10PF | 0.5PF | 50V |
| C611 | 1-135-259-11 | TANTALUM CHIP | 10uF | 20% | 6.3V | C833 | 1-164-850-11 | CERAMIC CHIP | 10PF | 0.5PF | 50V |
| C612 | 1-135-259-11 | TANTALUM CHIP | 10uF | 20% | 6.3V | C834 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C613 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | | | | | | |
| C614 | | CERAMIC CHIP | 0.1uF | 10% | 10V | C838 | 1-117-720-11 | CERAMIC CHIP | 4.7uF | | 10V |
| C615 | | CAP-CHIP | 100PF | 5% | 100V | C839 | | CERAMIC CHIP | 0.001uF | 10% | 50V |
| 0010 | 1 107 700 11 | 0/11 0/111 | 10011 | 0 70 | 1001 | C840 | 1-164-943-11 | CERAMIC CHIP | 0.00 rui 0.01 uF | 10% | 16V |
| C616 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C841 | | CERAMIC CHIP | 0.0047uF | 10% | 16V |
| C618 | | CERAMIC CHIP | 0.1uF | 10% | 10V 10V | C901 | | TANTALUM CHIP | | 20% | 6.3V |
| C619 | | CERAMIC CHIP | 0.1uF | 10% | 10V 10V | 0901 | 1-103-031-11 | TAINTALUWI GITT | Toul | 20 /0 | 0.5 V |
| | | | | | | 0000 | 1 105 051 11 | TANTAL LIM CLUD | 10 | 0.00/ | C OV |
| C621 | | CERAMIC CHIP | 0.01uF | 10% | 16V | C902 | | TANTALUM CHIP | | 20% | 6.3V |
| C622 | 1-131-862-11 | TANTALUM CHIP | 4/ur | 20% | 4V | C903 | | TANTALUM CHIP | | 20% | 6.3V |
| 0000 | 4 404 007 44 | 0504440 01115 | 0.004 5 | 400/ | 501/ | C904 | | TANTALUM CHIP | | 20% | 6.3V |
| C623 | | CERAMIC CHIP | 0.001uF | 10% | 50V | C905 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C624 | | TANTALUM CHIP | | 20% | 6.3V | C906 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V |
| C625 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V | | | | | | |
| C627 | | CERAMIC CHIP | 0.1uF | 10% | 10V | C907 | | CERAMIC CHIP | 2.2uF | 10% | 10V |
| C628 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C908 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| | | | | | | C909 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| C629 | 1-164-874-11 | CERAMIC CHIP | 100PF | 5% | 50V | C910 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| C630 | 1-128-829-11 | TANTALUM CHIP | 220uF | 20% | 6.3V | C911 | 1-135-259-11 | TANTALUM CHIP | 10uF | 20% | 6.3V |
| C631 | 1-124-413-31 | ELECT | 220uF | 20% | 4V | | | | | | |
| C632 | 1-135-259-11 | TANTALUM CHIP | 10uF | 20% | 6.3V | C912 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| C633 | | CERAMIC CHIP | 0.1uF | 10% | 10V | C913 | | CERAMIC CHIP | | 10% | 16V |
| 0000 | 1 120 777 11 | OZIU WIIO OIIII | o.rui | 1070 | 101 | C914 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C634 | 1_195_777_11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C915 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C635 | | CERAMIC CHIP | 0.1uF | 10% | 10V 10V | C916 | | CERAMIC CHIP | 1uF | 10% | 6.3V |
| | | | | | | 0910 | 1-120-03/-11 | OLIMANIO ONIP | rui" | I U /0 | U.J V |
| C636 | | CERAMIC CHIP | 0.1uF | 10% | 10V | 0017 | 1 105 777 11 | CEDAMIC CUID | 0.1 | 100/ | 101/ |
| C639 | | CERAMIC CHIP | 0.1uF | 10% | 10V | C917 | | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C701 | 1-137-739-11 | IANIALUM | 22uF | 20% | 6.3V | C918 | | CERAMIC CHIP | 2.2uF | 10% | 10V |
| c=== | | 0EB 4440 | 0.0005 | 1001 | 501 <i>:</i> | C919 | | TANTALUM CHIP | | 20% | 6.3V |
| C702 | | CERAMIC CHIP | 0.0022uF | 10% | 50V | C920 | | TANTALUM CHIP | | 20% | 6.3V |
| C703 | | CERAMIC CHIP | 0.001uF | 10% | 50V | C921 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V |
| C704 | 1-137-934-11 | TANTALUM CHIP | 47uF | 20% | 10V | | | | | | |
| C801 | 1-125-777-11 | CERAMIC CHIP | 0.1uF | 10% | 10V | C922 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| C802 | 1-164-943-11 | CERAMIC CHIP | 0.01uF | 10% | 16V | C924 | 1-164-937-11 | CERAMIC CHIP | 0.001uF | 10% | 50V |
| | | | | | | C926 | 1-165-851-11 | TANTALUM CHIP | 10uF | 20% | 6.3V |
| | | | | | | | | | | | |

MZ-N707

MAIN

| Ref. No. | Part No. | Description | | <u>Remark</u> | Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> |
|----------------|------------------------------|---------------------------------------|----------|----------------|------------------|------------------------------|--------------------|------------------------------------------|
| C927 | 1-125-777-11 | CERAMIC CHIP 0.1uF | 10% | 10V | | | | |
| C928 | 1-125-777-11 | CERAMIC CHIP 0.1uF | 10% | 10V | IC601 | | IC XPC18A22AF | |
| | | | | | IC604 | | IC XC61FS1YXX | |
| | | < CONNECTOR > | | | IC702 | | IC XC6367A361 | |
| CN501 | 1_573_360_21 | CONNECTOR, FFC/FPC 20 | D | | @ IC801 IC804 | | IC CXD2677-203 | |
| CN501 | | CONNECTOR, FFC/FPC 10 | | | 10004 | 0-739-393-47 | IC ARO417AWI-L | .2 |
| CN701 | | CONNECTOR (SQUARE T) | | 5P | IC901 | 8-759-698-61 | IC XPC18A32FC | R2 |
| | | (| | NNECTOR) | IC902 | | IC XC61CN0802 | |
| CN801 | 1-573-355-11 | CONNECTOR, FFC/FPC 15 | Р | | | | | |
| | | DIODE | | | | | < JACK > | |
| | | < DIODE > | | | 1201 | 1 015 050 11 | IACK /LINE IN /C | DTICAL \\ |
| D101 | 8-710-056-72 | DIODE 02DZ2.4-Z (TPH3 | 1 | | J301 J302 | 1-793-288-43 | JACK (LINE IN (C | JPTICAL)) |
| D201 | | DIODE 02DZ2.4-Z (TPH3 | | | J303 | | JACK (MIC (PLU) | G IN POWER)) |
| D301 | | DIODE NNCD6.8H-T1 | , | | J601 | | | RITY UNIFIED TYPE) |
| D601 | | DIODE MA2YD1500LS0 | | | | | | (DC IN 3V) |
| D602 | 8-719-081-33 | DIODE MA2YD1500LS0 | | | J602 | 1-815-912-21 | JACK, DC (FOR C | HARGING STAND) |
| DCOO | 0.740.004.00 | DIODE MAOVD4500100 | | | | | OOU /OUODT | |
| D603 D604 | | DIODE MA2YD1500LS0 DIODE MA2YD1500LS0 | | | | | < COIL/SHORT > | |
| D604 | | DIODE MA2YD1700LS0 | | | L301 | 1-414-398-11 | INDLICTOR | 10uH |
| D607 | | DIODE MA2YD1500LS0 | | | L303 | 1-414-398-11 | | 10uH |
| D608 | | DIODE MA2YD1700LS0 | | | L501 | 1-414-398-11 | INDUCTOR | 10uH |
| | | | | | L502 | 1-414-398-11 | | 10uH |
| D610 | | DIODE RB715W-TL | | | L551 | 1-216-296-11 | SHORT | 0 |
| D701 | | DIODE MA729-TX | | | 1.550 | 1 010 000 11 | CHODE | 0 |
| D703 D803 | | DIODE MA2YD1500LS0 DIODE MA8068-TX | | | L552 L553 | 1-216-296-11 1-216-296-11 | | 0 |
| D803 | | DIODE MA729-TX | | | L554 | 1-216-296-11 | | 0 |
| 5001 | 0710 120 01 | BIOBE WINTED IX | | | L601 | 1-428-912-21 | | 10uH |
| D901 | | DIODE MA2YD1500LS0 | | | L602 | 1-414-398-11 | | 10uH |
| D902 | | DIODE MA2YD1500LS0 | | | | | | |
| D903 | | DIODE MA729-TX | | | L701 | 1-469-952-21 | | 22uH |
| D904 | 8-719-404-50 | DIODE MA111-TX | | | L801 | 1-216-296-11 | | 0 |
| | | < FUSE > | | | L802 L901 | 1-469-535-21 1-419-953-21 | | 10uH 100uH |
| | | (100L) | | | L902 | 1-419-949-21 | | 22uH |
| £ F801 | 1-576-439-21 | FUSE (SMD) 0.25A | 125V | | | | | |
| | | | | | L904 | 1-414-398-11 | | 10uH |
| | | < FERRITE BEAD/RESISTO |)R > | | L905 | 1-469-426-21 | | 100uH |
| ER201 | 1-500-329-21 | EEDDITE | | | L906 | 1-469-367-21 | INDUCTOR | 10uH |
| FB302 | 1-216-864-11 | | 5% | 1/16W | | | < TRANSISTOR > | • |
| FB303 | 1-216-864-11 | | 5% | 1/16W | | | (111111010101017) | |
| FB304 | 1-216-864-11 | | 5% | 1/16W | Q301 | 8-729-037-52 | TRANSISTOR | 2SC4738F-Y/GR (TPL3) |
| FB305 | 1-216-864-11 | METAL CHIP 0 | 5% | 1/16W | Q302 | 8-729-051-23 | | 2SA2018TL |
| FD000 | | FERRITE | | | Q501 | 8-729-922-10 | | 2SA1577-T106-QR |
| FB306 | 1-469-084-21 | | E 0/ | 1/1C\M | Q601 | 8-729-046-45 | | SI2302DS-T1 |
| FB701 FB702 | 1-216-864-11 1-216-864-11 | | 5% 5% | 1/16W 1/16W | Q602 | 8-729-046-44 | INAIIOIOIUK | ZDT6718TA |
| FB703 | 1-216-864-11 | | 5% | 1/16W | Q603 | 8-729-053-71 | FET | TS8K1TB |
| FB704 | 1-216-864-11 | | 5% | 1/16W | Q604 | 8-729-046-43 | | HAT2051T-EL |
| | | | | | Q605 | 8-729-046-42 | | HAT2050T-EL |
| FB801 | 1-216-864-11 | | 5% | 1/16W | Q608 | 8-729-426-51 | | XP1210-TXE |
| FB802 | 1-216-864-11 | | 5% | 1/16W | Q701 | 8-729-053-03 | FEI | XP151A12A2MR |
| FB803 FB804 | 1-216-864-11 1-216-864-11 | | 5% 5% | 1/16W 1/16W | Q703 | 8-729-037-86 | TRANSISTOR | 2SB1462J-R (TX).S0 |
| FB805 | 1-216-864-11 | | 5% | 1/16W | Q703 | 8-729-041-51 | | FMMT617TA |
| | | - | | | Q705 | 8-729-047-48 | | UMD12N-TR |
| FB806 | 1-216-864-11 | METAL CHIP 0 | 5% | 1/16W | Q706 | 8-729-037-63 | | UN9115J-(TX).SO |
| | | | | | Q801 | 8-729-429-44 | TRANSISTOR | XP1501-TXE |
| | | < IC > | | | 0004 | 0 700 007 00 | TDANGICTOD | 00D0016 LD (TV) 00 |
| IC301 | 8-750-604-89 | IC AK5354VT-E2 | | | Q901 Q902 | 8-729-037-92 8-729-037-92 | | 2SD2216J-R (TX).S0 2SD2216J-R (TX).S0 |
| IC302 | | IC TA2131FL (EL) | | | Q903 | 8-729-037-92 | | 2SD2216J-R (TX).S0 |
| IC303 | | IC NJM2173AV (TE2) | | | Q904 | 8-729-034-59 | | 2SA1745-6.7-TL |
| IC501 | 6-701-391-01 | IC SN761057A | | | | | | |
| IC551 | 6-700-680-01 | IC SC111258FCR2 | | | l | | | |

[@] Replacement of CXD2677-202GA (IC801) used in this set requires a special tool.

MAIN

| | | | | | | | | | | L | 1417 111 4 |
|--------------|------------------------------|--------------|-----------|-------------|----------------|--------------|------------------------------|---------------------------------------|--------------|-------------|----------------|
| Ref. No. | Part No. | Description | | | Remark | Ref. No. | Part No. | Description | | | Remark |
| 1101. 110. | Turt No. | • | | | Homan | | | · · · · · · · · · · · · · · · · · · · | 4014 | 5 0/ | |
| | | < RESISTOR > | | | | R553 | 1-218-965-11 | | 10K | 5% | 1/16W |
| D101 | 1-218-969-11 | DEC CUID | 22K | 5% | 1/16W | R560 R561 | 1-218-957-11 1-218-957-11 | | 2.2K | 5% | 1/16W 1/16W |
| R101 R102 | 1-218-969-11 | | 10K | 0.5% | 1/16W | R562 | 1-218-957-11 | | 2.2K 2.2K | 5% 5% | 1/16W |
| | 1-208-707-11 | | 47K | | 1/16W | N302 | 1-210-937-11 | NES-UNIP | 2.2N | J 70 | 1/1000 |
| R103 R104 | | | | 5% | | R601 | 1-218-989-11 | DEC CHID | 1M | E 0/ | 1/16W |
| N 104 | 1-218-961-11 | NEO-UNIP | 4.7K | 5% | 1/16W | R602 | 1-218-981-11 | | 220K | 5% 5% | 1/16W |
| R104 | 1-218-965-11 | DEC CHID | 10K | 5% | (FR) 1/16W | R603 | 1-218-977-11 | | 100K | 5% 5% | 1/16W |
| n 104 | 1-210-905-11 | NEO-UNIF | IUK | | T US, FR) | R604 | 1-218-446-11 | | 1 | 5% 5% | 1/10W |
| | | | | (LXCL) | 1 03, 111) | R605 | 1-218-446-11 | | 1 | 5% | 1/10W |
| R104 | 1-218-969-11 | RES-CHIP | 22K | 5% | 1/16W | 11000 | 1-210-440-11 | WETAL OTH | 1 | J /0 | 1/1000 |
| 11104 | 1 210 303 11 | TILO OTTI | LLIN | 3 /0 | (US) | R606 | 1-218-949-11 | RES-CHIP | 470 | 5% | 1/16W |
| R105 | 1-218-961-11 | RES-CHIP | 4.7K | 5% | 1/16W | R607 | 1-218-945-11 | | 220 | 5% | 1/16W |
| R106 | 1-208-635-11 | | 10 | 5% | 1/16W | R608 | 1-218-981-11 | | 220K | 5% | 1/16W |
| R107 | 1-218-965-11 | | 10K | 5% | 1/16W | R609 | 1-219-724-11 | | 1 | 1% | 1/4W |
| R111 | 1-208-715-11 | | 22K | 0.5% | 1/16W | R610 | 1-218-969-11 | | 22K | 5% | 1/16W |
| | | | | | | | | | | | |
| R112 | 1-208-927-11 | METAL CHIP | 47K | 0.5% | 1/16W | R612 | 1-218-985-11 | RES-CHIP | 470K | 5% | 1/16W |
| R118 | 1-208-715-11 | METAL CHIP | 22K | 0.5% | 1/16W | R613 | 1-218-957-11 | RES-CHIP | 2.2K | 5% | 1/16W |
| R201 | 1-218-969-11 | RES-CHIP | 22K | 5% | 1/16W | R614 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W |
| R202 | 1-208-707-11 | METAL CHIP | 10K | 0.5% | 1/16W | R615 | 1-218-957-11 | RES-CHIP | 2.2K | 5% | 1/16W |
| R203 | 1-218-973-11 | RES-CHIP | 47K | 5% | 1/16W | R616 | 1-218-953-11 | RES-CHIP | 1K | 5% | 1/16W |
| | | | | | | | | | | | |
| R204 | 1-218-961-11 | RES-CHIP | 4.7K | 5% | 1/16W | R620 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W |
| | | | | | (FR) | R621 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W |
| R204 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W | R622 | 1-218-977-11 | RES-CHIP | 100K | 5% | 1/16W |
| | | | | (EXCEP | T US, FR) | R623 | 1-218-977-11 | RES-CHIP | 100K | 5% | 1/16W |
| R204 | 1-218-969-11 | RES-CHIP | 22K | 5% | 1/16W | R624 | 1-216-797-11 | METAL CHIP | 10 | 5% | 1/16W |
| | | | | | (US) | | | | | | |
| R205 | 1-218-961-11 | | 4.7K | 5% | 1/16W | R625 | 1-218-977-11 | | 100K | 5% | 1/16W |
| R206 | 1-208-635-11 | RES-CHIP | 10 | 5% | 1/16W | R626 | 1-216-789-11 | | 2.2 | 5% | 1/16W |
| | | | | | | R701 | 1-218-953-11 | | 1K | 5% | 1/16W |
| R207 | 1-218-965-11 | | 10K | 5% | 1/16W | R702 | 1-218-985-11 | | 470K | 5% | 1/16W |
| R211 | 1-208-715-11 | | 22K | 0.5% | 1/16W | R703 | 1-218-985-11 | RES-CHIP | 470K | 5% | 1/16W |
| R212 | 1-208-927-11 | | 47K | 0.5% | 1/16W | | | | _ | | |
| R218 | 1-208-715-11 | | 22K | 0.5% | 1/16W | R704 | 1-218-990-11 | | 0 | | |
| R302 | 1-218-953-11 | RES-CHIP | 1K | 5% | 1/16W | R705 | 1-218-957-11 | | 2.2K | 5% | 1/16W |
| D000 | 4 040 007 44 | DE0 0111D | 4-7 | 5 0/ | 4 (4 0) 14 | R706 | 1-218-977-11 | | 100K | 5% | 1/16W |
| R303 | 1-218-937-11 | | 47 | 5% | 1/16W | R707 | 1-218-961-11 | | 4.7K | 5% | 1/16W |
| R304 | 1-218-953-11 | | 1K | 5% | 1/16W | R708 | 1-218-985-11 | KES-CHIP | 470K | 5% | 1/16W |
| R305 | 1-218-953-11 1-218-969-11 | | 1K 22K | 5% 5% | 1/16W 1/16W | R709 | 1 010 041 11 | DEC CUID | 100 | 5% | 1/16W |
| R306 R307 | 1-218-941-11 | | 100 | 5% 5% | 1/16W 1/16W | R710 | 1-218-941-11 1-218-957-11 | | 2.2K | 5% 5% | 1/16W 1/16W |
| 11307 | 1-210-341-11 | NL3-OIIIF | 100 | J /0 | 1/1000 | R711 | 1-216-864-11 | | 0 | 5% | 1/16W |
| R308 | 1-218-990-11 | SHORT | 0 | | | R712 | 1-218-961-11 | | 4.7K | 5% | 1/16W |
| R309 | 1-218-981-11 | | 220K | 5% | 1/16W | R714 | 1-218-990-11 | | 0 | J /0 | 1/1000 |
| R310 | 1-218-990-11 | | 0 | 3 /0 | 1/1000 | 11717 | 1 210 330 11 | OHOITI | U | | |
| R311 | 1-218-989-11 | | 1M | 5% | 1/16W | R715 | 1-218-973-11 | RES-CHIP | 47K | 5% | 1/16W |
| R315 | 1-218-965-11 | | 10K | 5% | 1/16W | R801 | 1-218-981-11 | | 220K | 5% | 1/16W |
| | | | | - , - | ., | R802 | 1-208-903-11 | | 4.7K | 0.5% | 1/16W |
| R316 | 1-218-977-11 | RES-CHIP | 100K | 5% | 1/16W | R803 | 1-208-927-11 | | 47K | 0.5% | 1/16W |
| R317 | 1-218-941-11 | | 100 | 5% | 1/16W | R804 | 1-218-957-11 | | 2.2K | 5% | 1/16W |
| R318 | 1-218-953-11 | | 1K | 5% | 1/16W | | | | | | |
| R319 | 1-218-953-11 | | 1K | 5% | 1/16W | R805 | 1-218-957-11 | RES-CHIP | 2.2K | 5% | 1/16W |
| R320 | 1-218-965-11 | | 10K | 5% | 1/16W | R806 | 1-218-949-11 | | 470 | 5% | 1/16W |
| | | | | | | R807 | 1-208-635-11 | RES-CHIP | 10 | 5% | 1/16W |
| R501 | 1-218-973-11 | RES-CHIP | 47K | 5% | 1/16W | R808 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W |
| R502 | 1-218-981-11 | RES-CHIP | 220K | 5% | 1/16W | R809 | 1-218-977-11 | RES-CHIP | 100K | 5% | 1/16W |
| R503 | 1-218-985-11 | RES-CHIP | 470K | 5% | 1/16W | | | | | | |
| R505 | 1-208-707-11 | METAL CHIP | 10K | 0.5% | 1/16W | R810 | 1-218-977-11 | RES-CHIP | 100K | 5% | 1/16W |
| R507 | 1-218-953-11 | RES-CHIP | 1K | 5% | 1/16W | R814 | 1-208-635-11 | | 10 | 5% | 1/16W |
| | | | | | | R815 | 1-218-990-11 | | 0 | | |
| R517 | 1-208-683-11 | | 1K | 0.5% | 1/16W | R816 | 1-218-990-11 | | 0 | _ | |
| R519 | 1-218-977-11 | | 100K | 5% | 1/16W | R817 | 1-218-977-11 | RES-CHIP | 100K | 5% | 1/16W |
| R521 | 1-218-446-11 | | 1 | 5% | 1/10W | | 1 010 05: : | DE0 0:::5 | 000: | | 4 |
| R522 | 1-218-446-11 | | 1 | 5% | 1/10W | R821 | 1-218-981-11 | | 220K | 5% | 1/16W |
| R551 | 1-218-965-11 | KES-CHIP | 10K | 5% | 1/16W | R822 | 1-218-953-11 | | 1K | 5% | 1/16W |
| DECO | 1 010 005 11 | DEC CUID | 101/ | E0/ | 1/1014 | R823 | 1-218-945-11 | | 220 | 5% | 1/16W |
| R552 | 1-218-965-11 | NEO-UNIP | 10K | 5% | 1/16W | R824 | 1-218-957-11 | NEO-UHIP | 2.2K | 5% | 1/16W |
| | | | | | | | | | | | |

MZ-N707

MAIN

| Ref. No. | Part No. | Description | | | Remark | Ref. No. | Part No. | <u>Description</u> | | | <u>Remark</u> |
|----------|--------------|-------------|--------|-------------|-------------|-------------|---------------|----------------------|-------------|------------|---------------------------------------|
| R825 | 1-220-804-11 | RES-CHIP | 2.2M | 5% | 1/16W | R936 | 1-208-715-11 | METAL CHID | 22K | 0.5% | 1/16W |
| 11023 | 1-220-004-11 | NEO-OTHE | 2.2111 | J /0 | 1/1000 | | | | | | |
| D000 | 1 010 000 11 | OLIODT | • | | | R941 | 1-218-969-11 | | 22K | 5% | 1/16W |
| R826 | 1-218-990-11 | | 0 | | | R946 | 1-208-707-11 | | 10K | 0.5% | 1/16W |
| R827 | 1-216-864-11 | - | 0 | 5% | 1/16W | R947 | 1-208-683-11 | METAL CHIP | 1K | 0.5% | 1/16W |
| R828 | 1-208-691-11 | METAL CHIP | 2.2K | 0.5% | 1/16W | | | | | | |
| R829 | 1-208-699-11 | METAL CHIP | 4.7K | 0.5% | 1/16W | R948 | 1-208-935-11 | METAL CHIP | 100K | 0.5% | 1/16W |
| R830 | 1-218-989-11 | | 1M | 5% | 1/16W | | | | | | ., |
| 11000 | 1-210-303-11 | ILO-OIIII | IIVI | J /0 | 17 10 00 | | | < SWITCH > | | | |
| D004 | 1 010 000 11 | CHODT | 0 | | | | | < 30011011 > | | | |
| R831 | 1-218-990-11 | | 0 | | | _ | | | | | |
| R832 | 1-218-985-11 | RES-CHIP | 470K | 5% | 1/16W | S803 | 1-771-867-11 | SWITCH, PUSH (| (1 KEY) (PR | ROTECT D | ETECT) |
| R833 | 1-218-957-11 | RES-CHIP | 2.2K | 5% | 1/16W | S805 | 1-762-946-12 | SWITCH, PUSH (| 1 KEY) (OP | PEN) | |
| R834 | 1-208-935-11 | METAL CHIP | 100K | 0.5% | 1/16W | S806 | | SWITCH, PUSH (| | , | |
| R835 | 1-208-951-11 | | 470K | 0.5% | 1/16W | | | (| , | EN/CLOS | E DETECT) |
| 11000 | 1 200 001 11 | WEINE OITH | 17010 | 0.0 /0 | 17 10 11 | | | | (01 1 | LIW/ OLOGI | L DLILOI) |
| Dooc | 1 010 000 11 | CHODT | 0 | | | | | . CMI CUTCD . | | | |
| R836 | 1-218-990-11 | | 0 | | | | | < EMI FILTER > | | | |
| R838 | 1-218-949-11 | RES-CHIP | 470 | 5% | 1/16W | | | | | | |
| R839 | 1-218-989-11 | RES-CHIP | 1M | 5% | 1/16W | T601 | 1-416-405-21 | EMI FILTER | | | |
| R840 | 1-216-809-11 | METAL CHIP | 100 | 5% | 1/16W | | | | | | |
| R841 | 1-218-990-11 | | 0 | | | | | < THERMISTOR : | | | |
| 110-11 | 1 210 330 11 | OHOITI | O | | | | | < IIILIIIIIII III | | | |
| D0.40 | | METAL OLUB | • | 5 0/ | 4 /4 00 44 | T11004 | | THERMALOTOR R | 001711/5 | | |
| R843 | 1-216-864-11 | | 0 | 5% | 1/16W | TH601 | 1-804-616-21 | THERMISTOR, P | OSITIVE | | |
| R844 | 1-218-990-11 | SHORT | 0 | | | | | | | | |
| R845 | 1-208-643-11 | RES-CHIP | 22 | 5% | 1/16W | | | < VIBRATOR > | | | |
| R846 | 1-208-643-11 | | 22 | 5% | 1/16W | | | | | | |
| R847 | 1-218-990-11 | | 0 | 0 / 0 | 17 1011 | X801 | 1 705 004 11 | VIBRATOR, CRYS | STAL (45.16 | 50/MILI-\ | |
| N041 | 1-210-990-11 | SHUNI | U | | | | | | | | |
| | | | _ | | | X802 | | VIBRATOR, CRYS | | | |
| R849 | 1-218-990-11 | SHORT | 0 | | | ******* | ***** | ****** | ***** | ***** | ****** |
| R850 | 1-218-990-11 | SHORT | 0 | | | | | | | | |
| R851 | 1-218-990-11 | SHORT | 0 | | | | | MISCELLANEOUS | S | | |
| R852 | 1-208-931-11 | | 68K | 0.5% | 1/16W | | | ******* | | | |
| | | | | 0.5 /0 | 1/1000 | | | | | | |
| R853 | 1-218-990-11 | SHUKI | 0 | | | | | | | | |
| | | | | | | 4 | | LCD MODULE | | | |
| R855 | 1-218-990-11 | SHORT | 0 | | | △ 309 | X-3381-589-1 | SERVICE ASSY, (| OP (LCX-5P | R) | |
| R857 | 1-218-953-11 | RES-CHIP | 1K | 5% | 1/16W | M601 | 8-835-744-01 | MOTOR, DC SSN | 118B (SPIN | DLE) | |
| R858 | 1-218-989-11 | | 1M | 5% | 1/16W | | | , | • | , | RN TABLE) |
| | | | | | | Menn | 1 760 707 11 | MOTOD DC (CLE | , | | iiv iADEE) |
| R859 | 1-218-953-11 | | 1K | 5% | 1/16W | M602 | | MOTOR, DC (SLE | | | (5.014.41) |
| R860 | 1-218-953-11 | RES-CHIP | 1K | 5% | 1/16W | M603 | | MOTOR, DC (OVE | | | , |
| | | | | | | ******* | ********* | ****** | ******** | ****** | ***** |
| R861 | 1-218-953-11 | RES-CHIP | 1K | 5% | 1/16W | | | | | | |
| R863 | 1-218-977-11 | RFS-CHIP | 100K | 5% | 1/16W | | | ACCESSORIES | | | |
| R864 | 1-218-977-11 | | 100K | 5% | 1/16W | | | ******* | | | |
| | | | | | | | | | | | |
| R902 | 1-218-985-11 | | 470K | 5% | 1/16W | | | | | | |
| R903 | 1-218-957-11 | RES-CHIP | 2.2K | 5% | 1/16W | | 1-251-824-12 | CONNECTING PA | CK, CAR (C | ;PA-7) (U | S, CND) |
| | | | | | | | 1-476-211-22 | REMOTE CONTR | OL UNIT (R | M-MC10 | L/B) |
| R905 | 1-218-957-11 | RES-CHIP | 2.2K | 5% | 1/16W | | | | | (AE | P, UK, FR) |
| R906 | 1-218-990-11 | | 0 | | | | 1-476-303-11 | REMOTE CONTR | OL LINIT (B | | |
| | 1-218-989-11 | | 1M | E 0/ | 1/16W | | 1 470 000 11 | TILINIOTE CONTIN | OL OMIT (II | | |
| R907 | | | | 5% | | | 4 470 057 44 | 4 D 4 D T 0 D 4 0 /4 | 0 50005) (| | (US, CND) |
| R908 | 1-218-977-11 | | 100K | 5% | 1/16W | <u> </u> | | ADAPTOR, AC (A | / (| , , | |
| R909 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W | <u> </u> | 1-476-858-11 | ADAPTOR, AC (A | .C-ES305) (| UK) | |
| | | | | | | | | | | | |
| R910 | 1-218-965-11 | RES-CHIP | 10K | 5% | 1/16W | \triangle | 1-476-859-11 | ADAPTOR, AC (A | C-ES305) (| US CND |) |
| R911 | 1-218-949-11 | | 470 | 5% | 1/16W | | | BATTERY, NICKE | | | |
| | | | | | | | | | | | |
| R914 | 1-218-941-11 | | 100 | 5% | 1/16W | | | BATTERY, NICKE | | VI (US, UI | (טוי |
| R915 | 1-208-691-11 | METAL CHIP | 2.2K | 0.5% | 1/16W | <u> </u> | | STAND, CHARGE | | | |
| R916 | 1-218-981-11 | RES-CHIP | 220K | 5% | 1/16W | | 1-816-206-11 | CONNECTOR, LIC | GHT | | |
| | | | | | | | | | | | |
| R917 | 1-208-699-11 | METAL CHIP | 4.7K | 0.5% | 1/16W | | 1-823-510-11 | CORD, CONNECT | ION (IICE (| CARLE) | |
| R918 | 1-208-707-11 | | 10K | 0.5% | 1/16W | | | CASE, BATTERY | | | |
| | | | | | | | | | | | T IIO) |
| R919 | 1-208-927-11 | | 47K | 0.5% | 1/16W | | | CASE, BELT CLIP | | | |
| R920 | 1-208-707-11 | METAL CHIP | 10K | 0.5% | 1/16W | | 3-234-038-11 | MANUAL, INSTR | UCTION (EI | NGLISH) | (AEP, UK) |
| R921 | 1-218-985-11 | RES-CHIP | 470K | 5% | 1/16W | | | MANUAL, INSTR | | | |
| | | | | | | | | , | / | , (| · · · · · · · · · · · · · · · · · · · |
| DOOO | 1 010 000 11 | DEC CUID | 201/ | E 0/ | 1/16/4/ | | 0 004 000 04 | MANITAL INCTO | LICTION | | |
| R922 | 1-218-969-11 | | 22K | 5% | 1/16W | | J-2J4-UJÖ-J | MANUAL, INSTR | | ALL 1741 | AND (AED) |
| R923 | 1-218-981-11 | | 220K | 5% | 1/16W | | | • | MAN, DUT | υΗ, ΠALI | AN) (ALP) |
| R924 | 1-218-981-11 | RES-CHIP | 220K | 5% | 1/16W | | 3-234-038-41 | MANUAL, INSTR | UCTION | | |
| R925 | 1-218-981-11 | RES-CHIP | 220K | 5% | 1/16W | | | (S | PANISH, PO | ORTUGUE | SE) (AEP) |
| R927 | 1-218-989-11 | | 1M | 5% | 1/16W | | 3-234-038-51 | MANUAL, INSTR | | | |
| .1021 | . 2.0 000 11 | 31111 | | J /0 | ., | | 3 20 1 000 01 | | JU 1011 (U | | , |
| DOGG | 1 010 077 11 | חבכ כניים | 1001/ | E0/ | 4 /4 () [] | | | | | | (AEP) |
| R928 | 1-218-977-11 | KES-CHIP | 100K | 5% | 1/16W | I | | | | | |

The components identified by mark \triangle or dotted line with mark \triangle are critical for safety. Replace only with part number specified.

Les composants identifiés par une marque \triangle sont critiques pour la sécurité.
Ne les remplacer que par une pièce portant le numéro spécifié.

| Ref. No. | Part No. | <u>Description</u> | <u>Remark</u> |
|----------|------------------------------|-------------------------------------------------------------------------------------------------|----------------------|
| | 3-234-038-61 | MANUAL, INSTRUCTION (ENGLISH, | FRENCH) (US. CND) |
| | 3-237-042-11 | SOFT (CD-ROM), APPLICATION (US, | |
| | 3-237-042-21 3-239-304-11 | SOFT (CD-ROM), APPLICATION (AEP MANUAL, INSTRUCTION (How to Ins (Simple Burner) (ENGLISH) | tall) |
| | 3-239-304-21 | MANUAL, INSTRUCTION (How to Ins (Simple Burner) (FREN | tall) |
| | 3-239-305-11 | CARD (OPERATING INSTRUCTION) (Simple Burner) (ENGLISH) | |
| | 3-239-305-21 | CARD (OPERATING INSTRUCTION) (Simple Burner) (FREN | CH) (CND) |
| | 3-239-307-11 | MANUAL, INSTRUCTION (OpenMG Ji (ENGLISH) (US, CND | |
| | 3-239-307-21 | MANUAL, INSTRUCTION (OpenMG Jr (FRENCH) (CNE | ukebox) |
| | 3-239-307-31 | MANUAL, INSTRUCTION (OpenMG J | |
| | 3-239-307-41 | MANUAL, INSTRUCTION (OpenMG J | |
| | 3-239-307-51 | MANUAL, INSTRUCTION (OpenMG JI (DUT | ukebox) CH) (AEP) |
| | 3-239-307-61 | MANUAL, INSTRUCTION (OpenMG Jo | ukebox) SH) (AEP) |
| | 3-239-307-71 | MANUAL, INSTRUCTION (OpenMG J | |
| | 3-239-307-81 | MANUAL, INSTRUCTION (OpenMG J (PORTUGUE | ukebox) |
| | 3-239-307-91 | MANUAL, INSTRUCTION (OpenMG J | |
| | 3-241-715-01 | POUCH, CAR CARRYING (US, CND) | , \ , |
| | 8-954-007-94 8-954-008-91 | HEADPHONE MDR-027SP (US) RECEIVER, EAR MDR-E808SP (EXCE | PT US) |

REVISION HISTORY

Clicking the version allows you to jump to the revised page.

Also, clicking the version at the upper right on the revised page allows you to jump to the next revised page.

| Ver. | Date | Description of Revision |
|------|---------|-------------------------|
| 1.0 | 2002.01 | New |
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